

Proving the Correctness of the Standardized Algorithm for ABR Conformance

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Abstract. Conformance control for ATM cells is based on a real-time reactive algorithm which delivers a value depending on inputs from the network. This value must always fit with a well defined theoretical value. We present here the correctness proof of the algorithm standardized for the ATM transfer capability called ABR. The proof turned out to produce a key argument during the standardization process of ABR.

Keywords: specification, verification, reactive system, real-time, telecommunications.

Abbreviations: ABR – Available Bit Rate; ACR – Allowed Cell Rate; ATM – Asynchronous Transfer Mode; ATC – ATM Transfer Capability; GCRA – Generic control of Cell Rate Algorithm; DGCRA – Dynamic GCRA.

1. Introduction

We present in this paper an unusual (at least to our knowledge) application of formal methods in telecommunications, though closely related to a protocol. There is now quite a long tradition in this area of using formal languages, even standardized ones. They are based on communicating (extended) finite state machines (e.g. Estelle, SDL, Promela) or process algebras (e.g. Lotos). Verification based on model checking [19, 10] or simulation [15] has also been successfully employed. Typically, one models the protocol at hand, using one of the above formalisms, and then one tries to verify that bad things like unexpected messages, deadlocks and so on never happen. To achieve this effect one may use temporal logic formulas or observers and automated verification tools. This approach turns out to be very useful because the global behavior of a system made of several concurrent components is difficult to master.

In the problem we deal here with, complexity does not lie in parallelism or message interleaving, but in a single sequential, short, real-time and reactive algorithm. This algorithm runs on a key device for an ATM Transfer Capability called ABR (see below). It handles a small scheduler and delivers a value which depends on inputs from the network. We essentially have to prove that the value delivered by



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the device always agrees with (more precisely: is not smaller than) a theoretical value whose computation is not feasible under realistic assumptions. Several algorithms were studied at ITU-T, all of them involving tricky combinations of tests and updates. Our correctness proof has been a key argument in favor of one of them and freed the standardization process of ABR.

Let us emphasize that the input of the work reported here was just a piece of pseudo-code with diagrammatic and intuitive explanations on what happens in that or such case. Such hints turned out very incomplete and did not seem of much help for our purpose. We then decided to forget them and to carry out a proof on a purely technical ground, reducing the problem to small steps which are easy to verify—we are still unable to explain how the algorithm actually works. However we needed to understand how it *could* work, that is, what are the theoretical properties of an ideal version of the algorithm.

The technique used is basically the calculus of weakest preconditions of Dijkstra [11]. Indeed, the invariant provided in section 4.5 was obtained by successive strengthenings of the desired properties. Essentially, the algorithm runs transitions which are guarded assignments (also called *generalized substitutions* in B [2]) of the form **if** C **then** $x := E$, where C is a condition, x a variable (or a tuple of variables) and E is an expression. A property P is left invariant by such a transition if P implies the corresponding weakest precondition, which is denoted in the style of B by **[if** C **then** $x := E$] P and reduces to $C \Rightarrow [x := E]P$, where $[x := E]P$ is obtained by replacing every free occurrence of x with E in P . Roughly, the calculus consists in considering an invariant I and computing $[S]I$ for each generalized substitution S ; if $I \Rightarrow [S]I$, nothing has to be added at this stage, but in general I is not strong enough; $I \wedge [S]I$ is then the next candidate for the invariant. Eventually we reach a fixpoint where $I \Rightarrow [S]I$ for each S , provided enough information is embodied in the first version of I .

In our case, the invariant must express a property of the scheduler, which predicts values in the future and thus can be seen, abstractly, as a function of time: our invariant is a higher order property. Moreover, transitions are fired in real time: the current time appears as a free variable in the invariant and we have to explain how time evolves. This led us to a framework inspired by timed automata of Alur and Dill [4], involving two kinds of transitions: discrete transitions, which change the state and leave the current time invariant, and continuous transitions, which leave the state invariant while the time increases. In this way we get a model of the behaviour of the device under study, whose state, including a special variable called **ACR**, is a function of the time. The desired property involves another function of time called **Acr**, which is

defined in a simple and purely mathematical way by a specification \mathcal{S}_d . Our main result states that at any time t , $\text{Acr}(t)$ is less than or equal to $\text{ACR}(t)$.

In order to make the result as convincing as possible—a must in the context of standardization—it is important for the specification \mathcal{S}_d to be very simple and declarative (our proof steps are small and explicit for the same reason). We also explore consequences of \mathcal{S}_d : they provide insights on the behaviour of the algorithm under study. \mathcal{S}_d turns out technically not well suited to a direct correctness proof; however we can derive an equivalent but more tractable computational specification \mathcal{S}_c . The invariant to be proved is stated using \mathcal{S}_c ; the proof can then be carried out thanks to preliminary lemmas related to \mathcal{S}_c and formalized in COQ (an automated proof assistant based on type theory [6]), as reported in [16].

This case study illustrates that even on modest real-life examples, it may well appear that currently available ready-to-use formal methods are not able to cope with every aspect of the problem. However, we can use elementary mathematics to combine concepts coming from well-known formal methods and mechanically check the different steps.

The original work is published in [17]. Its formalization in COQ presented in [16] needed minor adaptations and corrections. We present here a new version of the proof which includes recent improvements. In particular, our treatment of time now conforms to the tradition initiated by Alur and Dill, at the price of somewhat subtle changes in the invariant. The new proof has again been completely checked in COQ.

The rest of this paper is organized as follows. Section 2 describes the problem as well as the stakes for telecommunications. Section 3 states the specifications called \mathcal{S}_d and \mathcal{S}_c above and explains how to get the latter from the former. Section 4 describes the state space with its invariant and sketches the main steps of the proof (the standardized algorithm and technical details of its correctness proof are respectively given in appendix A and B). We end with concluding remarks and related work in section 5.

2. Context and Motivation

2.1. CONFORMANCE CONTROL IN ATM

In an ATM (Asynchronous Transfer Mode) network, cells, i.e. data packets sent by a user, must not exceed a rate which is defined by a contract negotiated between the user and the network. Several modes

for using an ATM network, called “ATM Transfer Capabilities” (ATCs) have been defined. Each ATC may be seen as a generic contract between the user and the network, saying that the network must guarantee the negotiated *quality of service* (QoS), defined by a number of characteristics like maximum cell loss or transfer delay, provided the cells sent by the user conform to the negotiated traffic parameters (for instance, their rate must be bounded by some value). The conformance of cells sent by the user is checked using an algorithm called GCRA (generic control of cell rate algorithm). In this way, the network is protected against users misbehaviors and keeps enough resources for delivering the required QoS to well behaved users.

In fact, a new ATC cannot be accepted (as an international standard) without an efficient conformance control algorithm, and some evidence that this algorithm has the intended behavior.

For the ATC called ABR (Available Bit Rate), considered here, a simple but inefficient algorithm had been proposed in a first stage. Reasonably efficient algorithms proposed later turned out to be fairly complicated. This situation has been settled when one of them, due to Christophe Rabadan, has been proved correct in relation to the simple one: this algorithm is now part of the I.371.1 standard [1]. The first version of the proof was hand written. The main invariants discovered during this process are included in I.371.1.

2.2. THE CASE OF ABR

In some of the most recently defined ATCs, like ABR, the allowed cell rate may vary during the same session, depending on the current congestion state of the network. Such ATCs are designed for irregular sources, that need high cell rates from time to time, but that may reduce their cell rate when the network is busy. A servo-mechanism is then proposed in order to let the user know whether he can send data or not. This mechanism has to be well defined, in order to have a clear traffic contract between user and network. The key is an adaptation of the public algorithm for checking conformance of cells.

An abstract view of the protocol ABR is given in fig. 1 (actually, resource management (RM) cells are sent by the user, but only their transmission from the network to the user is relevant here; details are available in [20]). The conformance control algorithm for ABR has two parts. The first one is called DGCRA (dynamic GCRA). It just checks that the rate of data cells emitted by the user is not higher than a value called **ACR**, the allowed cell rate. Excess cells may be discarded by DGCRA. **ACR** is itself an approximation of an ideal allowed cell rate, which is denoted by *Acr*, but let us first consider that **ACR** and

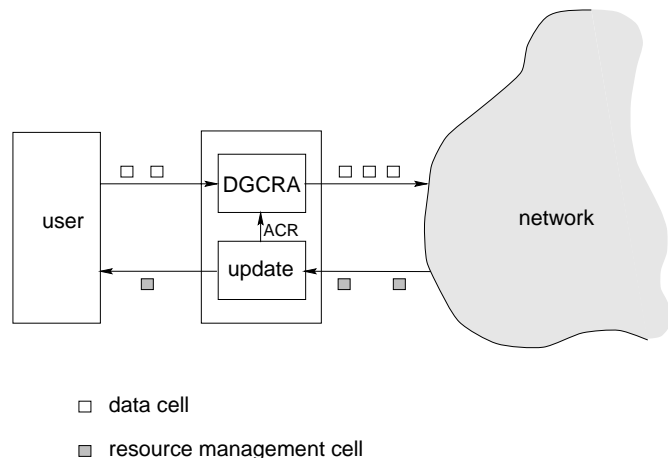


Figure 1. Conformance control

Acr are equal. In the case of ABR, Acr depends on time: its value has to be known each time a new data cell comes from the user. This part is quite simple and is not addressed here. The complexity lies in the computation of $Acr(t)$ (“update” in fig. 1), which depends on the sequence of values (ER_n) carried by resource management cells coming from the network. By a slight abuse of notation, the cell carrying ER_n will be called itself ER_n .

Of course, $Acr(t)$ depends only on cells ER_n whose arrival time t_n is such that $t_n < t$ (we order resource management cells so that $t_n < t_{n+1}$ for any n). In ABR, a resource management cell carries an intended allowed cell rate, that should be reached as soon as possible. At first sight, $Acr(t)$ should then be simply the last ER_i received at time t , i.e. ER_i with $i = \text{last}(t)$, where $\text{last}(x)$ is the only integer such that $t_i \leq x < t_{i+1}$. Unfortunately, because of electric propagation time and various transmission mechanisms, the user is aware of this expected value only after a delay. Taking the user’s reaction time τ observed by the control device into consideration, that is, the overall round trip time between the control device and the user, $Acr(t)$ should then be ER_i with $i = \text{last}(t - \tau)$. But τ may vary in turn. ITU-T considers that a lower bound τ_3 and an upper bound τ_2 for τ are established during the negotiation phase of each ABR connection. Hence, a cell arriving from the user at time t on DGCRA may legitimately have been emitted using any rate ER_i such that i is between $\text{last}(t - \tau_2)$ and $\text{last}(t - \tau_3)$ (see figure 2). Any rate less than or equal to any of these values, or, equivalently less than or equal to the maximum of them, should then be allowed. Therefore, $Acr(t)$ is taken as the maximum of these ER_i .

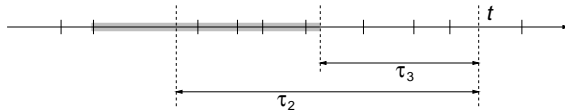


Figure 2. Cells to be taken into account at time t

Actually the standards committee did not give these explanations, but directly specified the set of ER_i under the equivalent form (2) below.

2.3. EFFECTIVE COMPUTATION OF $Acr(t)$

ITU-T committee considered that a direct computation of $Acr(t)$ is not feasible at reasonable cost with current technologies: it would amount to compute the maximum of several hundred integers each time a cell is received from the user. However, it is not difficult to see that $Acr(t)$ is constant on any interval that contains no value of the form $t_n + \tau_2$ or $t_n + \tau_3$. In other words, $Acr(t)$ is determined by a sequence of values. It then becomes possible to use a scheduler handling future changes of $Acr(t)$. This scheduler is updated when a new cell ER_n is received. Roughly, if s is the current time, ER_n will be taken into account at time $s + \tau_3$, while ER_{n-1} will not be taken into account after $s + \tau_2$.

The control conformance algorithm considered by ITU-T exploits this idea, with the further constraint that only a small amount of memory is allocated to the scheduler. This means that some information is lost. *Filtering is performed in such a way that the actual value of the allowed cell rate, as implemented by a variable called ACR , is greater or equal to its theoretical value $Acr(t)$ defined above.*

3. Ideal Allowed Cell Rate

3.1. DECLARATIVE SPECIFICATION

The declarative specification (\mathcal{S}_d in the introduction) of the ideal value or Acr is given by (1) and (2) under assumption (3). We are given a sequence of RM cells (ER_i) whose arrival time are respectively (t_i); the desired allowed cell rate at time t is defined by:

$$Acr(t) = \max\{ER_i \mid i \in I(t)\}, \quad (1)$$

where I is the interval defined by:

$$i \in I(t) \quad \text{iff} \quad (t - \tau_2 < t_i \leq t - \tau_3) \vee (t_i \leq t - \tau_2 < t_{i+1}). \quad (2)$$

The second disjunct of (2) means $i = \text{last}(t - \tau_2)$. The t_i are taken in increasing order:

$$t_1 < t_2 < \dots < t_n < \dots \quad (3)$$

This specification was officially provided to us by ITU-T, but the reader can check that (2) is equivalent to:

$$i \in I(t) \quad \text{iff} \quad \text{last}(t - \tau_2) \leq i \leq \text{last}(t - \tau_3) .$$

The following equivalent characterization of $I(t)$ is easier to handle:

$$i \in I(t) \quad \text{iff} \quad t_i + \tau_3 \leq t < t_{i+1} + \tau_2 \quad (4)$$

The initial (inefficient) ABR conformance control algorithm was a direct computation of Acr according to (1).

3.2. SPECIFICATION USING ONLY FINITE KNOWLEDGE

In practice, only finite prefixes of the sequence (t_i) are available. Then we have to take into account that, after the reception of the n first RM cells (ER_i), whose arrival time are respectively (t_i) , t_{i+1} makes sense only for $i < n$. However, if we consider that $t_{n+1} = \infty$, (4) boils down to $n \in I(t)$ iff $t_n \leq t - \tau_3$. With this intuition in mind we introduce the n^{th} approximation of Acr , defined by

$$\text{Approx}(n, t) = \max\{\text{ER}_i \mid i \in I_a(n, t)\}, \quad (5)$$

where $I_a(n, t)$ is similar to $I(t)$:

$$i \in I_a(n, t) \quad \text{iff} \quad \begin{cases} i \in I(t) & \wedge \quad i < n \\ \vee \\ t_i \leq t - \tau_3 & \wedge \quad i = n . \end{cases} \quad (6)$$

The number n we consider depends on time : if s represents the current time, we have $i \leq n(s)$ if and only if $t_i \leq s$. The following lemma, whose meaning is that it is enough to compute $\text{Approx}(n(s), t)$, is easy to prove :

Lemma 1.

(i) The value of Approx at t becomes stable after $t - \tau_3$:

$$\forall s, t - \tau_3 \leq s \Rightarrow \quad \text{Approx}(n(s), t) = \text{Approx}(n(t - \tau_3), t) .$$

(ii) $\text{Approx}(n(s), t)$ is an exact approximation of $\text{Acr}(t)$ for $t - \tau_3 \leq s$:

$$\forall t, \quad \text{Acr}(t) = \text{Approx}(n(t - \tau_3), t) .$$

Unless otherwise stated s will remain implicit in the following : we will write just n instead of $n(s)$. In the same way, variables like \mathbf{Efi} (see below) actually handled by the algorithm denote a value that also depends on s , but will be noted just \mathbf{Efi} . We also assume without loss of generality that ER_0 is equal to the initial value of Acr ; if the algorithm starts at s_0 , this amounts to stating $t_0 = s_0 - \tau_3$ and for i and t_i such that $0 < i$ and $s_0 < t_i$:

$$t_0 = s_0 - \tau_3 < s_0 < t_1 < t_2 < \dots < t_n . \quad (7)$$

The will use the following explicit characterization of $I_a(n, t)$:

$$i \in I_a(n, t) \text{ iff } \begin{cases} t_i + \tau_3 \leq t < t_{i+1} + \tau_2 & \wedge \quad i < n \\ \vee \\ t_i + \tau_3 \leq t & \wedge \quad i = n . \end{cases} \quad (8)$$

In particular, for $t_n + \tau_3 \leq t \leq t'$, we have $i \in I_a(n, t')$ implies $i \in I_a(n, t)$, hence the following lemma :

Lemma 2. $\text{Approx}(n, t)$ is decreasing after $t_n + \tau_3$:

$$\forall t, t', t_n + \tau_3 \leq t \leq t' \Rightarrow \text{Approx}(n, t') \leq \text{Approx}(n, t) .$$

It is also easy to see that for any t greater than $t_n + \tau_2$, the only i of $I_a(n, t)$ is n , hence the following lemma :

Lemma 3.

$$\forall t, \quad t_n + \tau_2 \leq t \quad \Rightarrow \quad i \in I_a(n, t) \text{ iff } i = n, \quad (9)$$

$$\forall t, \quad t_n + \tau_2 \leq t \quad \Rightarrow \quad \text{Approx}(n, t) = \text{ER}_n. \quad (10)$$

3.3. COMPUTING Approx IN AN INCREMENTAL WAY

Initially (at time $s_0 = t_0 + \tau_3$), we have $n = 0$. The characterization (8) of I_a yields then

$$i \in I_a(0, t) \quad \text{iff} \quad s_0 \leq t \wedge i = 0 .$$

Then we get (as desired):

$$\forall t, \quad s_0 \leq t \Rightarrow \text{Approx}(0, t) = \text{ER}_0 . \quad (11)$$

We now consider the arrival of ER_n . Let us fix a given t .

- If $t_n \leq t - \tau_2$, lemma 3 yields $I_a(n, t) = \{n\}$ and $\text{Approx}(n, t) = \text{ER}_n$.

- If $t - \tau_2 < t_n \leq t - \tau_3$, $I_a(n, t)$ includes n as well as the numbers already in $I_a(n - 1, t)$: this can be shown from (8) and is intuitively clear from figure 2. We get $I_a(n, t) = I_a(n - 1, t) \cup \{n\}$ and $\text{Approx}(n, t) = \max(\text{Approx}(n - 1, t), \text{ER}_n)$
- In the same way, if $t - \tau_3 < t_{n+1}$, then ER_{n+1} is outside the relevant interval for t , which yields $I_a(n+1, t) = I_a(n, t)$ and $\text{Approx}(n, t) = \text{Approx}(n - 1, t)$

Thus we get the following way of computing the value of $\text{Approx}(n, t)$ from the value of $\text{Approx}(n - 1, t)$ (this is what we called \mathcal{S}_c in the introduction).

Lemma 4. The value of $\text{Approx}(n, t)$ is given by the following table.

$t < t_n + \tau_3$	$t_n + \tau_3 \leq t < t_n + \tau_2$	$t_n + \tau_2 \leq t$
$\text{Approx}(n - 1, t)$	$\max(\text{Approx}(n - 1, t), \text{ER}_n)$	ER_n

A simple but useful consequence of lemma 4 is :

Lemma 5.

$$\forall l, t, t_n, \text{ER}_n, M, \\ \text{Approx}(l, t) \leq M \wedge \text{ER}_n \leq M \quad \Rightarrow \quad \text{Approx}(l \frown t_n, t) \leq M. \quad (12)$$

4. Proof of Algorithm B'

The enhanced algorithm B (hereafter called B') proposed by France Télécom at ITU-T computes an upper bound of Approx . More precisely, at the current instant s , the state $e(s)$ handled by B' defines a function Ub of $e(s)$ and t such that $\text{Ub}(e(s), t)$ is greater than $\text{Approx}(n(s), t)$ for any $t \geq s$ and is not defined for $t < s$.

Running the algorithm consists of changing the current state e to a new state e' . Such a step is called a *discrete transition*. Algorithm B' defines two discrete transitions: the first, called the external transition, is fired when receiving a new RM cell (this is called an external event in the sequel); the second, called the internal transition, is fired when the current time reaches the time for a scheduled event (this is called an internal event in the sequel).

4.1. MODELLING AND PROOF PRINCIPLES

Our treatment of time is inspired by timed automata of [4] and the synchrony hypothesis of synchronous languages [13]. Timed automata distinguish “continuous” transitions concerning time evolution (modeled by clocks under the control of the environment) and “discrete” transitions changing the state using no time.

Considering that discrete transitions are instantaneous deserves a special discussion. Basically, we assume, as in synchronous languages, that our system reacts more quickly than its environment. This assumption depends on the technology used in the real device and can be checked on it. It is then equivalent and simpler to consider that a discrete transition takes no time: if the property of interest is true on a model where discrete transitions are instantaneous, it remains true if the exact duration of discrete transitions are taken into account. Here, the internal transition is simply a multiple assignment; moreover, its crucial part reduces to a single assignment about **ACR**. The most complicated transition is the external one; but our discussion on *Approx* showed that the arrival of a new RM cell at time t_k has an effect only after $t_k + \tau_3$ (see lemma 4). Thus it is enough that running the external transition takes less than τ_3 . It may happen that an internal event is scheduled at a time t_k . In that case, the internal event has to be handled first.

Concerning continuous transitions, we just need to assume the existence of a clock s . We model the progress of time by an assignment $s := s'$.

A run is a sequence of pairs $\langle s_i, e_i \rangle$ where s_i the i^{th} value of the clock, and e_i is the i^{th} value of the state, with the condition that for all i , we have $s_i \leq s_{i+1}$ and $e_i = e_{i+1}$ (continuous transition) or $s_i = s_{i+1}$ and e_{i+1} is obtained from e_i by applying an internal or an external transition. In the case of a continuous transition, we also constrain s_{i+1} in a way such that no event arose between s_i and s_{i+1} (we assume that the scheduler is reliable).

4.2. NOTATIONS

States and discrete transitions are modelled using standard calculus of weakest preconditions [11] with notations taken from B [2]: discrete transitions are modeled by program assignments or “generalized substitutions” in the terminology of B, as discussed in the introduction. We use multiple assignments like $x_1, x_2 := E_1, E_2$. Applying $[x_1, x_2 := E_1, E_2]$ on a predicate P yields P where x_1 and x_2 are simultaneously replaced with E_1 and E_2 . Another notation for the same multiple assignment is $x_1 := E_1 \parallel x_2 := E_2$.

4.3. COMPONENTS OF THE STATE

The state e is made of 5 variables :

- **ACR**, the current Allowed Cell Rate;
- **Efi**, the next Allowed Cell Rate if nothing new happens;
- **tfi**, the time at which the Allowed Cell Rate will become equal to **Efi** if nothing new happens;
- **Ela**, containing the value of the last known cell (ER_n);
- **tla**, the time at which the Allowed Cell Rate will become equal to **Ela** if nothing new happens.

As an optimization trick, there is a sixth variable **Emx** whose value is just the maximum of **Efi** and **Ela**.

When running a continuous transition from $\langle s, e \rangle$ to $\langle s', e \rangle$, we cannot have $s < u < s'$ where u is **tfi** or one of the t_n . But continuous transitions can follow discrete transitions and vice-versa, hence we must allow $s = u$ and $u = s'$. It turns out that the desired property is not satisfied when $s' = \mathbf{tfi}$, that is just before running the internal transition: an internal event is precisely scheduled in order to update the allowed cell rate at this time. In order to take this into account, our model involves an additional boolean variable **utd** (for “up to date”): **utd** is true most of the time; it becomes false only after a continuous transition such that $s' = \mathbf{tfi}$. Note that, **utd** is not part of the algorithm, it is just an artefact of the model which is needed in the invariant.

4.4. TRANSITIONS

The algorithm reacts either when receiving a new ER_n , i.e. when the current time is equal to t_n , or when the current time reaches **tfi**. Each transition changes the current state; an internal event is scheduled if and only if **tfi** is greater than the current time.

4.5. INVARIANT

We want to ensure that algorithm B' provides an **ACR** which cannot be less than the ideal value $Acr(s)$. To this effect we prove that the following property is invariant, where s denotes the current time and n denotes the number of the last RM cell received at time s :

$$\mathbf{utd} = \text{true} \Rightarrow \text{Approx}(n, s) \leq \mathbf{ACR} . \quad (I_{\text{main}})$$

I_{main} is itself a consequence of the following conjunction.

$$\begin{aligned}
& \text{utd} = \text{false} \Rightarrow s = \text{tfi} && (I_{\text{utd}}) \\
& \text{Emx} = \max(\text{Efi}, \text{Ela}) && (I_{\text{max}}) \\
& \text{Ela} = \text{ER}_n && (I_{\text{Ela}}) \\
& \text{tfi} \leq \text{tla} \leq t_n + \tau_2 && (I_{\text{fil}}) \\
& (\text{tfi} = s \Rightarrow \text{utd} = \text{true}) \Rightarrow && (I_{\text{tfs}}) \\
& \text{tfi} \leq s \Rightarrow \forall t, s \leq t \Rightarrow \text{Approx}(n, t) \leq \text{ACR} \\
& \text{ACR} < \text{Efi} \Rightarrow \text{tfi} \leq t_n + \tau_3 && (I_{\text{Et1}}) \\
& \text{Efi} < \text{Ela} \Rightarrow \text{tla} \leq t_n + \tau_3 && (I_{\text{Et2}}) \\
& \text{tfi} = \text{tla} \Rightarrow \text{Efi} = \text{Ela} && (I_{\text{tE}}) \\
& \forall t \quad s \leq t < \text{tfi} \Rightarrow \text{Approx}(n, t) \leq \text{ACR} && (I_{\text{Ub1}}) \\
& \forall t \quad \text{tfi} \leq t < \text{tla} \Rightarrow \text{Approx}(n, t) \leq \text{Efi} && (I_{\text{Ub2}}) \\
& \forall t \quad \text{tla} \leq t \Rightarrow \text{Approx}(n, t) \leq \text{Ela} . && (I_{\text{Ub3}})
\end{aligned}$$

We define

$$\text{Inv} = I_{\text{utd}} \wedge I_{\text{max}} \wedge I_{\text{Ela}} \wedge I_{\text{fil}} \wedge I_{\text{tfs}} \wedge I_{\text{Et1}} \wedge I_{\text{Et2}} \wedge I_{\text{tE}} \wedge I_{\text{Ub1}} \wedge I_{\text{Ub2}} \wedge I_{\text{Ub3}}.$$

Invariants I_{Ub1} , I_{Ub2} and I_{Ub3} mean that $\text{Approx}(n, t) \leq \text{Ub}(e, t)$ for $t \geq s$, where the function $\text{Ub}(e, t)$ is defined by: $\text{Ub}(e, t) = \text{ACR}$ for $s \leq t < \text{tfi}$, $\text{Ub}(e, t) = \text{Efi}$ for $\text{tfi} \leq t < \text{tla}$, $\text{Ub}(e, t) = \text{Ela}$ for $\text{tla} \leq t$. In the sequel we use the following consequence of I_{max} , I_{Ub2} and I_{Ub3} :

$$\forall t \quad \text{tfi} \leq t \Rightarrow \text{Approx}(n, t) \leq \text{Emx} . \quad (I_{\text{Apx}})$$

Lemma 6. Inv implies I_{main} .

Proof. We have either $\text{tfi} \leq s$ or $s < \text{tfi}$. Apply respectively I_{tfs} and I_{Ub1} with $t = s$.

4.6. INITIAL STATE

Initially we have $n = 0$ and we want to show that Inv is true in the initial state defined by:

$$\text{tfi} = \text{tla} = s_0, \text{ACR} = \text{Emx} = \text{Efi} = \text{Ela} = \text{ER}_0 \quad (\text{initial value of Acr}),$$

and $\mathbf{utd} = \mathbf{true}$ Formally, we consider the substitution S_0 :

$$S_0 \stackrel{\text{df}}{=} \left\{ \begin{array}{l} n, \mathbf{ACR}, \mathbf{Emx}, \mathbf{Efi}, \mathbf{Ela}, \mathbf{tfi}, \mathbf{tla}, \mathbf{utd} \\ 0, \mathbf{ER}_0, \mathbf{ER}_0, \mathbf{ER}_0, \mathbf{ER}_0, s_0, s_0, \mathbf{true} \end{array} \right.$$

and we show $[S_0]\text{Inv}$, that is, the formula Inv where $n, \mathbf{ACR}, \mathbf{Emx}, \mathbf{Efi}, \mathbf{Ela}, \mathbf{tfi}, \mathbf{tla}$ are respectively replaced with $0, \mathbf{ER}_0, \mathbf{ER}_0, \mathbf{ER}_0, \mathbf{ER}_0, s_0, s_0$. The proof is very easy.

4.7. CONTINUOUS TRANSITIONS

Let s be the current time. We consider a transition from $\langle s, e \rangle$ to $\langle s', e \rangle$ only if $s \leq s'$ and there is no event between s and s' . If s' is equal to \mathbf{tfi} , \mathbf{utd} is set to false (when real time reaches \mathbf{tfi} , S_i must run). In the same way, if $s = t_{n+1}$, time cannot progress (an external transition must run, $n := n + 1$).

Formally, we consider the following guard:

$$\begin{aligned} s \leq s' \wedge (\mathbf{tfi} \leq s \vee s' \leq \mathbf{tfi}) \wedge (\forall i, t_i \leq s \vee s' \leq t_i) \wedge \\ (\mathbf{utd} = \mathbf{false} \Rightarrow s' = \mathbf{tfi}) \wedge s = t_{n+1} \Rightarrow s' = t_{n+1}. \end{aligned} \quad (G_c)$$

The transition is modeled by the substitution:

$$S_c \stackrel{\text{df}}{=} s, \mathbf{utd} := s', \mathbf{newutd}$$

with $\mathbf{newutd} = \mathbf{true}$ if $s' \neq \mathbf{tfi}$ and $\mathbf{newutd} = \mathbf{false}$ if $s' = \mathbf{tfi}$. We prove: $\text{Inv} \wedge (G_c) \Rightarrow [S_c]\text{Inv}$.

Remark 1. We consider proof obligations of the form $[S]\text{Inv}$, where $[S]$ is a substitution. It is decomposed into $[S]I_{\text{utd}}$, $[S]I_{\text{max}}$, $[S]I_{\text{Ela}}$, $[S]I_{\text{fil}}$, $[S]I_{\text{tfs}}$, $[S]I_{\text{Et1}}$, $[S]I_{\text{Et2}}$, $[S]I_{\text{tE}}$, $[S]I_{\text{Ub1}}$, $[S]I_{\text{Ub2}}$ and $[S]I_{\text{Ub3}}$. Some of them are immediate, for instance $\mathbf{Efi} < \mathbf{ER}_k \Rightarrow t_k + \tau_3 \leq t_k + \tau_3$ or $\mathbf{Ela} < \mathbf{Ela} \Rightarrow \mathbf{tla} \leq t_n + \tau_3$. They are skipped in order to save space.

Proof.

- $[S_c]I_{\text{tfs}}$, that is $(\mathbf{tfi} = s' \Rightarrow \mathbf{newutd} = \mathbf{true}) \Rightarrow \mathbf{tfi} \leq s' \Rightarrow \forall t, s' \leq t \Rightarrow \text{Approx}(n, t) \leq \mathbf{ACR}$: first remark that if $\mathbf{tfi} = s'$, we get $\mathbf{newutd} = \mathbf{true}$ which is $s' \neq \mathbf{tfi}$, hence a contradiction; this implies $\mathbf{utd} = \mathbf{true}$ (because otherwise, $\mathbf{utd} = \mathbf{false}$ and then $\mathbf{tfi} = s'$ by (G_c)) and also that $\mathbf{tfi} \leq s'$ reduces to $\mathbf{tfi} < s'$; using (G_c) we then get $\mathbf{tfi} \leq s$; for t such that $s' \leq t$, we have $s \leq t$ by (G_c) ; then we get $\text{Approx}(n, t) \leq \mathbf{ACR}$ thanks to I_{tfs} .
- $[S_c]I_{\text{Ub1}}$, that is $\forall t s' \leq t < \mathbf{tfi} \Rightarrow \text{Approx}(n, t) \leq \mathbf{ACR}$: for t such that $s' \leq t < \mathbf{tfi}$ we have $s \leq s' \leq t < \mathbf{tfi}$ by (G_c) , then $\text{Approx}(n, t) \leq \mathbf{ACR}$ by I_{Ub1} .

4.8. INTERNAL TRANSITION

Let s be the current time with $s = \mathbf{tfi}$. Let S_i be the substitution

$$S_i \stackrel{\text{df}}{=} \text{ACR}, \mathbf{tfi}, \mathbf{Efi}, \mathbf{Emx}, \mathbf{utd} := \mathbf{Efi}, \mathbf{tla}, \mathbf{Ela}, \mathbf{Ela}, \text{true}.$$

We show: $\text{Inv} \wedge s = \mathbf{tfi} \Rightarrow [S_i]\text{Inv}$.

Remark 2. $[S_i]I_{\text{tfs}}$ has the form $(X \Rightarrow \text{true} = \text{true}) \Rightarrow Y$: this reduces to Y . The same remark applies on the external transition, because the considered substitution includes $\mathbf{utd} := \text{true}$.

Proof.

- $[S_i]I_{\text{Ela}}$, that is $\mathbf{Ela} = \text{ER}_n$: from I_{Ela} .
- $[S_i]I_{\text{fil}}$, that is $\mathbf{tla} \leq \mathbf{tla} \leq t_n + \tau_2$: we have $\mathbf{tla} \leq t_n + \tau_2$ by I_{fil} .
- $[S_i]I_{\text{tfs}}$, that is $\mathbf{tla} \leq \mathbf{tfi} \Rightarrow \forall t, \mathbf{tfi} \leq t \Rightarrow \text{Approx}(n, t) \leq \mathbf{Efi}$: $\mathbf{tla} \leq \mathbf{tfi}$ and I_{fil} yields $\mathbf{tfi} = \mathbf{tla}$, then $\mathbf{Efi} = \mathbf{Ela}$ by I_{tE} ; we then have to show that for t such that $\mathbf{tla} \leq t$, we have $\text{Approx}(n, t) \leq \mathbf{Ela}$: we just apply I_{Ub3} .
- $[S_i]I_{\text{Et1}}$, that is $\mathbf{Efi} < \mathbf{Ela} \Rightarrow \mathbf{tla} \leq t_n + \tau_3$: it is just I_{Et2} .
- $[S_i]I_{\text{Ub1}}$, that is $\forall t \mathbf{tfi} \leq t < \mathbf{tla} \Rightarrow \text{Approx}(n, t) \leq \mathbf{Efi}$: it is just I_{Ub2} .
- $[S_i]I_{\text{Ub3}}$, that is $\forall t \mathbf{tla} \leq t \Rightarrow \text{Approx}(n, t) \leq \mathbf{Ela}$: for t such that $\mathbf{tla} \leq t$, we have $s \leq \mathbf{tla} \leq t$ by (G_i) ; we can then apply I_{Ub3} , which yields $\text{Approx}(n, t) \leq \mathbf{Ela}$.

4.9. EXTERNAL TRANSITION

Let $s = t_k$ be the current time, and let $n = k - 1$ be the number of the last RM cell received before s . We consider a transition taking the k^{th} RM cell only if \mathbf{utd} is true. Formally, we have the following guard:

$$\mathbf{utd} = \text{true} \quad (G_e)$$

The complete external substitution is divided into eight cases, depending on comparisons between ER_k , ACR , \mathbf{Efi} , \mathbf{Ela} on the one hand, and between $t_k + \tau_2$ or $t_k + \tau_3$ and \mathbf{tfi} and \mathbf{tla} on the other hand. In each case, the considered substitution includes:

$$\mathbb{T}_e \stackrel{\text{df}}{=} n, \mathbf{utd} := k, \text{true}.$$

For instance, consider the eightth case:

if $t_k < \mathbf{tfi}$ **then else if** $\mathbf{ACR} \leq \mathbf{ER}_k$ **then else**
 $\mathbf{Efi} := \mathbf{ER}_k$ || $\mathbf{Ela} := \mathbf{ER}_k$ || $\mathbf{Emx} := \mathbf{ER}_k$
 || $\mathbf{tfi} := t_k + \tau_2$ || $\mathbf{tla} := t_k + \tau_2$

This transitions corresponds to the case where the scheduler is empty (\mathbf{tfi} is not greater than the current time and \mathbf{utd} is true) and where a cell smaller than the current ACR is received: \mathbf{ER}_k is then scheduled at $t_k + \tau_2$. Let \mathbf{Sg} be the substitution

$$\mathbf{Sg} \stackrel{\text{df}}{=} \mathbf{T}_e \parallel \mathbf{Efi}, \mathbf{Ela}, \mathbf{Emx}, \mathbf{tfi}, \mathbf{tla} := \mathbf{ER}_k, \mathbf{ER}_k, \mathbf{ER}_k, t_k + \tau_2, t_k + \tau_2.$$

This transition is correct if :

$$\text{Inv} \wedge (G_e) \wedge \mathbf{tfi} \leq t_k \wedge \mathbf{ER}_k < \mathbf{ACR} \Rightarrow [\mathbf{Sg}]\text{Inv}.$$

The complete pseudo-code and the proofs are given in appendixes A and B.

4.10. MAIN THEOREM

Our main result is an easy consequence of previous lemmas.

THEOREM 1. *At any time s we have $\text{Acr}(s) \leq \mathbf{ACR}$.*

Proof. Using lemma 1 we know that $\text{Acr}(s) = \text{Approx}(n, s)$. As Inv is actually an invariant, lemma 6 yields $\text{Approx}(n, s) \leq \mathbf{ACR}$ when $\mathbf{utd} = \text{true}$. However if $\mathbf{utd} = \text{false}$, we have $s = \mathbf{tfi}$ by $\text{I}_{\mathbf{utd}}$ and the internal transition is immediately fired, hence the result.

4.11. FURTHER PROPERTIES

It is easy to see that at any time, the guard of at least one transition is satisfied. Moreover, time progress is never blocked: continuous transitions with a strictly positive duration are allowed, excepted when the current time is equal to t_k , or to \mathbf{tfi} with \mathbf{utd} equal to false. In the latter case, an internal transition can be fired, making \mathbf{utd} true. When \mathbf{utd} is true, the current time is either equal to t_k or not. In the first case, an external transition can be fired, making the current time strictly less than t_k because n is incremented; \mathbf{utd} remains true, hence continuous transitions with a strictly positive duration are allowed again.

The model given above also allows stuttering transitions, *i.e.* a continuous transitions with a null duration ($s' = s$). A fairness condition is then needed in order to ensure actual progress. An alternative is to replace $s \leq s'$ with $s < s'$ in (G_c) .

An interesting consequence of progress and of Inv is that the standardized algorithm is actually much better than the trivial algorithm which just returns the maximum of the ER_k . Indeed, if no RM cell is received before $\mathbf{t1a}$, the value of ACR at $\mathbf{t1a}$ will be $E1a$: by I_{fil} we know that $\mathbf{t1a}$ cannot be later than $s + \tau_2$ and by I_{E1a} , $E1a$ is equal to the last received ER_n . Moreover, in this situation, ER_n is precisely equal to $Acr(t)$ for t greater than $s + \tau_2$.

5. Discussion and Related Work

For engineers working in the context of standardization, theorem 1 is much more convincing than the similar theorem involving Approx instead of Acr. However it is clear for us that the computational characterization of Approx (lemma 4) is much more suited for reasoning about algorithm B'. In a first attempt, we tried to prove directly the invariant Inv using (1) and (2). This resulted in shallow areas and even holes in the manual proof.

We also submitted the problem of the correctness of B' to other research teams, in order to assess other approaches. It is too early (and beyond the scope of this paper) to compare the results of these works, we just give some hints. Model checking using classical and temporal automata is experimented in the framework of FORMA¹, a project founded by the French government which aims at experimenting various formal methods on industrial case studies. In the two first attempts, the property to be checked corresponded to theorem 1, but modeling Acr contributed to an explosion of the number of states. Moreover the tools used—UPPAAL [7] and MEC [5]—allowed only fixed numeric values for τ_2 , τ_3 and ER_i . Checking the algorithm could be carried through for small values. Later on, good results within two different frameworks have been obtained by L. Fribourg [12] and B. Bérard [8], with specifications based on Approx instead of Acr. In one framework, they used the parameterized temporized automata of Hytech [14], and in the other an automated proof search procedure due to Revesz [21] was extended to timed automata. In both cases τ_2 , τ_3 , etc. were symbolic parameters and the desired property could be checked without the help of Inv. In our case, Inv has been incrementally constructed while attempting to prove I_{tfs} and I_{Ubl} , following the steps given in appendix B. Note that such calculations are boring and error prone: this is why we felt that the proof should be checked with a proof assistant. Indeed, our experiment with COQ [16] showed that one of the proofs of appendix B was wrong

¹ <http://www-verimag.imag.fr/FORMA>

(but could be repaired, fortunately !). A detailed comparison between the approaches mentioned above is done in [9].

A similar algorithm for ABR conformance is studied in [22]. This algorithm handles a non bounded scheduling list. Its proof, which involves intricate case analysis and inductive reasoning on lists, has been supported by the PVS [18] theorem prover. The main design decision was to write a first order specification in order to exploit the automated features of PVS. However the proof still requires around 80 lemmas. The authors hope that many steps could be simplified with the use of more automated tools. Another direction, more akin to the spirit of the work presented in this paper, would be to carry out a proof using higher-order specification and reasoning techniques, in order to derive a proof that is more synthetic and therefore easier to grasp.

Finally, let us say a word on two attempts using B. Three years ago, we (with G. Blorec) tried to use this method on this case study. At first sight B should be well suited, because of our systematic use of substitution calculus. But we failed to handle time and the very notion of scheduler in a nice way; our specification was heavy and many proof obligations could not be discharged. Recently, Abrial worked on this problem using an event oriented variant of B and he succeeded to reconstruct an algorithm different from the one standardized in I.371, but where the design decisions are much clearer [3].

Our current feeling is that specialized procedures or methods can discharge boring and painful parts in the verification process, but are really successful only on “predigested” specifications like \mathcal{S}_c , in contrast with \mathcal{S}_d . On the other side, general purpose frameworks and tools like type theory and COQ are helpful on the whole process but still require much more interaction from the user on the parts automatically handled by specialized methods. Work is in progress for integrating timed automata generalized with arbitrary types (including e.g. function) and automated techniques within the same tool, in the framework of a research project partially founded by the french government (Calife).

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Appendix

A. Pseudo-code for Algorithm B'

When real time reaches t_k :

```

if  $t_k < \mathbf{tfi}$  then
  if  $\mathbf{Emx} \leq \mathbf{ER}_k$  then
    if  $\mathbf{tfi} < t_k + \tau_3$  then
      if  $t_k + \tau_3 < \mathbf{tla} \vee \mathbf{tfi} = \mathbf{tla}$  then
         $\mathbf{Emx} := \mathbf{ER}_k \parallel \mathbf{Ela} := \mathbf{ER}_k \parallel \mathbf{tla} := t_k + \tau_3$ 
      else
         $\mathbf{Emx} := \mathbf{ER}_k \parallel \mathbf{Ela} := \mathbf{ER}_k$ 
    else
      if  $\mathbf{ACR} \leq \mathbf{ER}_k$  then
         $\mathbf{Emx} := \mathbf{ER}_k \parallel \mathbf{Efi} := \mathbf{ER}_k \parallel \mathbf{Ela} := \mathbf{ER}_k$ 
         $\parallel \mathbf{tfi} := t_k + \tau_3 \parallel \mathbf{tla} := t_k + \tau_3$ 
      else
         $\mathbf{Emx} := \mathbf{ER}_k \parallel \mathbf{Efi} := \mathbf{ER}_k \parallel \mathbf{Ela} := \mathbf{ER}_k \parallel \mathbf{tla} := \mathbf{tfi}$ 
    else
      if  $\mathbf{ER}_k < \mathbf{Ela}$  then
         $\mathbf{Efi} := \mathbf{Emx} \parallel \mathbf{Ela} := \mathbf{ER}_k \parallel \mathbf{tla} := t_k + \tau_2$ 
      else
         $\mathbf{Efi} := \mathbf{Emx} \parallel \mathbf{Ela} := \mathbf{ER}_k$ 
    else
      if  $\mathbf{ACR} \leq \mathbf{ER}_k$  then
         $\mathbf{Efi} := \mathbf{ER}_k \parallel \mathbf{Ela} := \mathbf{ER}_k \parallel \mathbf{Emx} := \mathbf{ER}_k$ 
         $\parallel \mathbf{tfi} := t_k + \tau_3 \parallel \mathbf{tla} := t_k + \tau_3$ 
      else
         $\mathbf{Efi} := \mathbf{ER}_k \parallel \mathbf{Ela} := \mathbf{ER}_k \parallel \mathbf{Emx} := \mathbf{ER}_k$ 
         $\parallel \mathbf{tfi} := t_k + \tau_2 \parallel \mathbf{tla} := t_k + \tau_2$ 

```

When real time reaches \mathbf{tfi} :

$\mathbf{ACR} := \mathbf{Efi} \parallel \mathbf{tfi} := \mathbf{tla} \parallel \mathbf{Efi} := \mathbf{Ela} \parallel \mathbf{Emx} := \mathbf{Ela}$

If $\mathbf{tfi} = t_k$, we run the algorithm for \mathbf{tfi} , then the algorithm for t_k .

B. Proof of Algorithm B'

Recall that (G_e) is $\text{utd} = \text{true}$

CASE 1

if $t_k < \text{tfi}$ **then if** $\text{Emx} \leq \text{ER}_k$ **then if** $\text{tfi} < t_k + \tau_3$
then if $t_k + \tau_3 < \text{tla} \vee \text{tfi} = \text{tla}$
then $\text{Emx} := \text{ER}_k \parallel \text{Ela} := \text{ER}_k \parallel \text{tla} := t_k + \tau_3$

Let S_1 be the substitution $S_1 \stackrel{\text{df}}{=} \tau_e \parallel \text{Emx}, \text{Ela}, \text{tla} := \text{ER}_k, \text{ER}_k, t_k + \tau_3$.
This transition is correct if :

$$\begin{aligned}
& \text{Inv} \quad \wedge \quad (G_e) \quad \wedge \\
& \qquad \qquad \qquad t_k < \text{tfi} \quad \wedge \qquad \qquad \qquad (G_{11}) \\
& \qquad \qquad \qquad \text{Emx} \leq \text{ER}_k \quad \wedge \qquad \qquad \qquad (G_{12}) \\
& \qquad \qquad \qquad \text{tfi} < t_k + \tau_3 \quad \wedge \qquad \qquad \qquad (G_{13}) \\
& (t_k + \tau_3 < \text{tla} \vee \text{tfi} = \text{tla}) \quad \Rightarrow \qquad \qquad \qquad (G_{14}) \\
& \qquad \qquad \qquad [S_1]\text{Inv}.
\end{aligned}$$

Proof. We assume Inv , (G_e) , (G_{11}) , (G_{12}) , (G_{13}) , (G_{14}) , and we prove $[S_1]\text{Inv}$.

- $[S_1]I_{\text{max}}$, that is $\text{ER}_k = \max(\text{Efi}, \text{ER}_k)$: by I_{max} and (G_{12}) .
- $[S_1]I_{\text{fil}}$, that is $\text{tfi} \leq t_k + \tau_3 \leq t_k + \tau_2$: trivial from (G_{13}) .
- $[S_1]I_{\text{tfs}}$, that is $\text{tfi} \leq t_k \Rightarrow \forall t, t_k \leq t \Rightarrow \text{Approx}(k, t) \leq \text{ACR}$: absurd hypothesis, given (G_{11}) .
- $[S_1]I_{\text{Et1}}$, that is $\text{ACR} < \text{Efi} \Rightarrow \text{tfi} \leq t_k + \tau_3$: the conclusion comes from (G_{13}) .
- $[S_1]I_{\text{ttE}}$, that is $\text{tfi} = t_k + \tau_3 \Rightarrow \text{Efi} = \text{ER}_k$: absurd hypothesis, given (G_{13}) .
- $[S_1]I_{\text{Ub1}}$, that is $\forall t, t_k \leq t < \text{tfi} \Rightarrow \text{Approx}(k, t) \leq \text{ACR}$:
for t such that $t_k \leq t < \text{tfi}$, we get $t < t_k + \tau_3$ by (G_{13}) , then lemma 4 yields $\text{Approx}(k, t) = \text{Approx}(k-1, t)$; we can then apply I_{Ub1} , and finally we get $\text{Approx}(k, t) = \text{Approx}(k-1, t) \leq \text{ACR}$.
- $[S_1]I_{\text{Ub2}}$, that is $\forall t, \text{tfi} \leq t < t_k + \tau_3 \Rightarrow \text{Approx}(k, t) \leq \text{Efi}$:
first remark that $\text{Approx}(k, t) = \text{Approx}(k-1, t)$ by lemma 4; (G_{14}) gives either $t_k + \tau_3 < \text{tla}$, or $\text{tfi} = \text{tla}$;

- in the former case, $\text{Approx}(k-1, t) \leq \mathbf{Efi}$ by $I_{\text{Ub}2}$, hence the result;
 - in the latter case, I_{ttE} yields $\mathbf{Efi} = \mathbf{Ela}$, and $\mathbf{tla} = \mathbf{tfi} \leq t$ yields $\text{Approx}(k-1, t) \leq \mathbf{Ela}$ by $I_{\text{Ub}3}$, hence the result.
- $[S_1]I_{\text{Ub}3}$, that is $\forall t, t_k + \tau_3 \leq t \Rightarrow \text{Approx}(k, t) \leq \text{ER}_k$:
we show $\forall t, t_k + \tau_3 \leq t \Rightarrow \text{Approx}(k, t) = \text{ER}_k$:
for t such that $t_k + \tau_3 \leq t$, we have $\mathbf{tfi} \leq t$ by (G_{13}) , then $\text{Approx}(k-1, t) \leq \mathbf{Emx} \leq \text{ER}_k$ by I_{ApX} and (G_{12}) ; lemma 4 yields either $\text{Approx}(k, t) = \max(\text{Approx}(k-1, t), \text{ER}_k)$ or $\text{Approx}(k, t) = \text{ER}_k$; in both cases, we see that $\text{Approx}(k, t) = \text{ER}_k$.

CASE 2

if $t_k < \mathbf{tfi}$ then if $\mathbf{Emx} \leq \text{ER}_k$ then if $\mathbf{tfi} < t_k + \tau_3$ then
if $t_k + \tau_3 < \mathbf{tla} \vee \mathbf{tfi} = \mathbf{tla}$ then
else $\mathbf{Emx} := \text{ER}_k \parallel \mathbf{Ela} := \text{ER}_k$

nLet S_2 be the substitution $S_2 \stackrel{\text{df}}{=} T_e \parallel \mathbf{Emx}, \mathbf{Ela} := \text{ER}_k, \text{ER}_k$.
This transition is correct if :

$$\begin{array}{l}
\text{Inv} \quad \wedge \quad (G_e) \quad \wedge \\
t_k < \mathbf{tfi} \quad \wedge \quad (G_{11}) \\
\mathbf{Emx} \leq \text{ER}_k \quad \wedge \quad (G_{12}) \\
\mathbf{tfi} < t_k + \tau_3 \quad \wedge \quad (G_{13}) \\
\mathbf{tla} \leq t_k + \tau_3 \quad \wedge \quad (G_{24}) \\
\mathbf{tfi} \neq \mathbf{tla} \quad \Rightarrow \quad (G_{25}) \\
[S_2]\text{Inv}.
\end{array}$$

Proof. $[S_2]I_{\text{max}}$, $[S_2]I_{\text{tfs}}$ and $[S_2]I_{\text{Et}1}$, are proved as in case 1.

- $[S_2]I_{\text{fil}}$, that is $\mathbf{tfi} \leq \mathbf{tla} \leq t_k + \tau_2$: trivial from I_{fil} (G_{24}) .
- $[S_2]I_{\text{Et}2}$, that is $\mathbf{Efi} < \text{ER}_k \Rightarrow \mathbf{tla} \leq t_k + \tau_3$: the conclusion is (G_{24}) .
- $[S_2]I_{\text{ttE}}$, that is $\mathbf{tfi} = \mathbf{tla} \Rightarrow \mathbf{Efi} = \text{ER}_k$: absurd hypothesis, given (G_{25}) .
- $[S_2]I_{\text{Ub}1}$, that is $\forall t, t_k \leq t < \mathbf{tfi} \Rightarrow \text{Approx}(k, t) \leq \text{ACR}$: using (G_{13}) , lemma 4 and $I_{\text{Ub}1}$, we have $\text{Approx}(k, t) = \text{Approx}(k-1, t) \leq \text{ACR}$.

- $[S_2]_{I_{Ub2}}$, that is $\forall t, \mathbf{tfi} \leq t < \mathbf{tla} \Rightarrow \text{Approx}(k, t) \leq \mathbf{Efi}$:
 $\text{Approx}(k, t) = \text{Approx}(k-1, t)$ by lemma 4 and (G_{24}) ;
 $\text{Approx}(k-1, t) \leq \mathbf{Efi}$ by I_{Ub2} , hence the result.
- $[S_2]_{I_{Ub3}}$, that is $\forall t, \mathbf{tla} \leq t \Rightarrow \text{Approx}(k, t) \leq \mathbf{ER}_k$: we have $\mathbf{tfi} \leq t$
by I_{fi1} , then $\text{Approx}(k-1, t) \leq \mathbf{Emx} \leq \mathbf{ER}_k$ by I_{Apx} and (G_{12}) ; taking
 $M = \mathbf{ER}_k$ in lemma 5 gives $\text{Approx}(k, t) \leq \mathbf{ER}_k$.

CASE 3

if $t_k < \mathbf{tfi}$ **then if** $\mathbf{Emx} \leq \mathbf{ER}_k$ **then if** $\mathbf{tfi} < t_k + \tau_3$ **then**
else if $\mathbf{ACR} \leq \mathbf{ER}_k$ **then**
 $\mathbf{Emx} := \mathbf{ER}_k \parallel \mathbf{Efi} := \mathbf{ER}_k \parallel \mathbf{Ela} := \mathbf{ER}_k$
 $\parallel \mathbf{tfi} := t_k + \tau_3 \parallel \mathbf{tla} := t_k + \tau_3$

Let S_3 be the substitution

$S_3 \stackrel{\text{df}}{=} T_e \parallel \mathbf{Efi}, \mathbf{Ela}, \mathbf{Emx}, \mathbf{tfi}, \mathbf{tla} := \mathbf{ER}_k, \mathbf{ER}_k, \mathbf{ER}_k, t_k + \tau_3, t_k + \tau_3.$

This transition is correct if :

$$\begin{aligned}
& \text{Inv} \quad \wedge \quad (G_e) \quad \wedge \\
& \qquad \qquad \qquad t_k < \mathbf{tfi} \quad \wedge \qquad \qquad \qquad (G_{11}) \\
& \qquad \qquad \qquad \mathbf{Emx} \leq \mathbf{ER}_k \quad \wedge \qquad \qquad \qquad (G_{12}) \\
& \qquad \qquad \qquad t_k + \tau_3 \leq \mathbf{tfi} \quad \wedge \qquad \qquad \qquad (G_{33}) \\
& \qquad \qquad \qquad \mathbf{ACR} \leq \mathbf{ER}_k \quad \Rightarrow \qquad \qquad \qquad (G_{34}) \\
& \qquad \qquad \qquad [S_3]\text{Inv}.
\end{aligned}$$

Proof.

- $[S_3]_{I_{Ub1}}$, that is $\forall t, t_k \leq t < t_k + \tau_3 \Rightarrow \text{Approx}(k, t) \leq \mathbf{ACR}$: using
lemma 4 and I_{Ub1} , we have $\text{Approx}(k, t) = \text{Approx}(k-1, t) \leq \mathbf{ACR}$.
- $[S_3]_{I_{Ub3}}$, that is $\forall t, t_k + \tau_3 \leq t \Rightarrow \text{Approx}(k, t) \leq \mathbf{ER}_k$: we show
 $\forall t, t_k + \tau_3 \leq t \Rightarrow \text{Approx}(k, t) = \mathbf{ER}_k$; we have either $t < \mathbf{tfi}$ or
 $\mathbf{tfi} \leq t$;
 - in the first case, $\text{Approx}(k-1, t) \leq \mathbf{ACR} \leq \mathbf{ER}_k$ by I_{Ub1} ; here
 $t_k \leq t$ comes from $t_k \leq t_k + \tau_3 \leq t$ and (G_{34}) ;
 - in the second case, $\text{Approx}(k-1, t) \leq \mathbf{Emx} \leq \mathbf{ER}_k$ by I_{Apx} and
 (G_{12}) ;

hence $\text{Approx}(k-1, t) \leq \text{ER}_k$ is always true; using lemma 4 we get $\text{Approx}(k, t) = \text{ER}_k$ for t such that $t_k + \tau_3 \leq t$.

CASE 4

if $t_k < \mathbf{tfi}$ **then if** $\text{Emx} \leq \text{ER}_k$
then if $\mathbf{tfi} < t_k + \tau_3$ **then**
else $\text{Emx} := \text{ER}_k$ **||** $\text{Efi} := \text{ER}_k$ **||** $\text{Ela} := \text{ER}_k$ **||** $\mathbf{tla} := \mathbf{tfi}$

Let S_4 be the substitution

$$S_4 \stackrel{\text{df}}{=} T_e \text{ || } \text{Efi}, \text{Ela}, \text{Emx}, \mathbf{tla} := \text{ER}_k, \text{ER}_k, \text{ER}_k, \mathbf{tfi}.$$

This transition is correct if :

$$\begin{aligned} \text{Inv} \quad \wedge \quad (G_e) \quad \wedge \\ t_k < \mathbf{tfi} \quad \wedge \quad (G_{11}) \\ \text{Emx} \leq \text{ER}_k \quad \wedge \quad (G_{12}) \\ t_k + \tau_3 \leq \mathbf{tfi} \quad \wedge \quad (G_{33}) \\ \text{ER}_k < \text{ACR} \quad \Rightarrow \quad (G_{44}) \\ [S_4]\text{Inv}. \end{aligned}$$

Proof.

- $[S_4]I_{\text{fi1}}$, that is $\mathbf{tfi} \leq \mathbf{tfi} \leq t_k + \tau_2$:
we have $\mathbf{tfi} \leq t_n + \tau_2 = t_{k-1} + \tau_2 \leq t_k + \tau_2$ by I_{fi1} , definition of n and (7).
- $[S_4]I_{\text{tfs}}$, that is $\mathbf{tfi} \leq t_k \Rightarrow \forall t, t_k \leq t \Rightarrow \text{Approx}(k, t) \leq \text{ACR}$:
hypothesis absurd, given (G_{11}) .
- $[S_4]I_{\text{Et1}}$, that is $\text{ACR} < \text{ER}_k \Rightarrow \mathbf{tfi} \leq t_k + \tau_3$: absurd hypothesis,
given (G_{44}) .
- $[S_4]I_{\text{Ub1}}$, that is $\forall t, t_k \leq t < \mathbf{tfi} \Rightarrow \text{Approx}(k, t) \leq \text{ACR}$:
by I_{Ub1} and $t < \mathbf{tfi}$, we have $\text{Approx}(k-1, t) \leq \text{ACR}$; taking $M = \text{ACR}$ in lemma 5 and using (G_{44}) yields $\text{Approx}(k, t) \leq \text{ACR}$.
- $[S_4]I_{\text{Ub3}}$, that is $\forall t, \mathbf{tfi} \leq t \Rightarrow \text{Approx}(k, t) \leq \text{ER}_k$: we show
 $\forall t, \mathbf{tfi} \leq t \Rightarrow \text{Approx}(k, t) = \text{ER}_k$; for t such that $\mathbf{tfi} \leq t$, we
have
 $\text{Approx}(k-1, t) \leq \text{Emx} \leq \text{ER}_k$ by I_{ApX} and (G_{12}) ; we have $t_k +$

$\tau_3 \leq \mathbf{tfi} \leq t$ by (G_{33}) , then lemma 4 yields either $\text{Approx}(k, t) = \max(\text{Approx}(k-1, t), \text{ER}_k)$
or $\text{Approx}(k, t) = \text{ER}_k$; in both cases, we see that $\text{Approx}(k, t) = \text{ER}_k$.

CASE 5

if $t_k < \mathbf{tfi}$ **then if** $\text{Emx} \leq \text{ER}_k$ **then else if** $\text{ER}_k < \text{Ela}$
then $\text{Efi} := \text{Emx}$ **||** $\text{Ela} := \text{ER}_k$ **||** $\mathbf{tla} := t_k + \tau_2$

Let S_5 be the substitution

$$S_5 \stackrel{\text{df}}{=} T_e \text{ || } \text{Efi}, \text{Ela}, \mathbf{tla} := \text{Emx}, \text{ER}_k, t_k + \tau_2.$$

This transition is correct if :

$$\begin{aligned} \text{Inv} \quad \wedge \quad (G_e) \quad \wedge \\ t_k < \mathbf{tfi} \quad \wedge \quad (G_{11}) \\ \text{ER}_k < \text{Emx} \quad \wedge \quad (G_{52}) \\ \text{ER}_k < \text{Ela} \quad \Rightarrow \quad (G_{53}) \\ [S_5]\text{Inv}. \end{aligned}$$

Proof. $[S_5]I_{\text{fil}}$ and $[S_5]I_{\text{tfs}}$ are similar to $[S_4]I_{\text{fil}}$ and $[S_4]I_{\text{tfs}}$

- $[S_5]I_{\text{max}}$, that is $\text{Emx} = \max(\text{Emx}, \text{ER}_k)$: by (G_{52}) .
- $[S_5]I_{\text{Et1}}$, that is $\text{ACR} < \text{Emx} \Rightarrow \mathbf{tfi} \leq t_k + \tau_3$: we have $\text{Efi} < \text{Ela}$ or $\text{Ela} \leq \text{Efi}$;
 - in the first case, $\mathbf{tfi} \leq \mathbf{tla} \leq t_n + \tau_3$ by I_{fil} and I_{Et2} ;
 - in the second case, $\text{Emx} = \text{Efi}$, then $\mathbf{tfi} \leq t_n + \tau_3$ by I_{Et1} and I_{max} ;
in both cases, $\mathbf{tfi} \leq t_{k-1} + \tau_3 \leq t_k + \tau_3$ by definition of n and (7).
- $[S_5]I_{\text{Et2}}$, that is $\text{Emx} < \text{ER}_k \Rightarrow t_k + \tau_2 \leq t_k + \tau_3$:
the hypothesis $\text{Emx} < \text{ER}_k$ is absurd given (G_{52}) .
- $[S_5]I_{\text{ttE}}$, that is $\mathbf{tfi} = t_k + \tau_2 \Rightarrow \text{Emx} = \text{ER}_k$:
we have $\mathbf{tfi} \leq t_n + \tau_2 = t_{k-1} + \tau_2 < t_k + \tau_2$ by I_{fil} , definition of n and (7); then the hypothesis $\mathbf{tfi} = t_k + \tau_2$ is absurd.
- $[S_5]I_{\text{Ub1}}$, that is $\forall t, t_k \leq t < \mathbf{tfi} \Rightarrow \text{Approx}(k, t) \leq \text{ACR}$:
by I_{Ub1} and $t < \mathbf{tfi}$, we have $\text{Approx}(k-1, t) \leq \text{ACR}$; we also have $\mathbf{tfi} \leq t_k + \tau_3$ or $t_k + \tau_3 < \mathbf{tfi}$;

- in the first case, $\text{Approx}(k, t) = \text{Approx}(k-1, t) \leq \text{ACR}$ by lemma 4;
 - in the second case, $\text{ER}_k < \text{Emx} \leq \text{ACR}$ by (G_{52}) and contraposition of $[\text{S}_5]_{\text{I}_{\text{Et}1}}$ shown above; taking $M = \text{ACR}$ in lemma 5 yields $\text{Approx}(k, t) \leq \text{ACR}$.
- $[\text{S}_5]_{\text{I}_{\text{Ub}2}}$, that is $\forall t, \text{tfi} \leq t < t_k + \tau_2 \Rightarrow \text{Approx}(k, t) \leq \text{Emx}$:
for t such that $\text{tfi} \leq t$, we have $\text{Approx}(k-1, t) \leq \text{Emx}$, by I_{Apx} ; taking $M = \text{Emx}$ in lemma 5 and using (G_{52}) yields $\text{Approx}(k, t) \leq \text{Emx}$.
- $[\text{S}_5]_{\text{I}_{\text{Ub}3}}$, that is $\forall t, t_k + \tau_2 \leq t \Rightarrow \text{Approx}(k, t) \leq \text{ER}_k$:
by lemma 4, we have $\forall t, t_k + \tau_2 \leq t \Rightarrow \text{Approx}(k, t) = \text{ER}_k$.

CASE 6

**if $t_k < \text{tfi}$ then if $\text{Emx} \leq \text{ER}_k$ then else if $\text{ER}_k < \text{Ela}$ then
else $\text{Efi} := \text{Emx} \parallel \text{Ela} := \text{ER}_k$**

Let S_6 be the substitution

$$\text{S}_6 \stackrel{\text{df}}{=} \text{T}_e \parallel \text{Efi}, \text{Ela} := \text{Emx}, \text{ER}_k.$$

This transition is correct if :

$$\begin{aligned} \text{Inv} \quad \wedge \quad (G_e) \quad \wedge \\ t_k < \text{tfi} \quad \wedge \quad (G_{11}) \\ \text{ER}_k < \text{Emx} \quad \wedge \quad (G_{52}) \\ \text{Ela} \leq \text{ER}_k \quad \Rightarrow \quad (G_{63}) \\ [\text{S}_6]_{\text{Inv}}. \end{aligned}$$

Proof. $[\text{S}_6]_{\text{I}_{\text{Et}1}}$ and $[\text{S}_6]_{\text{I}_{\text{Et}2}}$ are similar to $[\text{S}_5]_{\text{I}_{\text{Et}1}}$ and $[\text{S}_5]_{\text{I}_{\text{Et}2}}$.

- $[\text{S}_6]_{\text{I}_{\text{max}}}$, that is $\text{Emx} = \max(\text{Emx}, \text{ER}_k)$: by (G_{52}) .
- $[\text{S}_6]_{\text{I}_{\text{fil}}}$, that is $\text{tfi} \leq \text{tla} \leq t_k + \tau_2$: we have $\text{tfi} \leq \text{tla} \leq t_n + \tau_2 = t_{k-1} + \tau_2 \leq t_k + \tau_2$ by I_{fil} , definition of n and (7).
- $[\text{S}_6]_{\text{I}_{\text{tfs}}}$, that is $\text{tfi} \leq t_k \Rightarrow \forall t, t_k \leq t \Rightarrow \text{Approx}(k, t) \leq \text{ACR}$:
the hypothesis is absurd given (G_{11}) .

- $[S_6]I_{ttE}$, that is $\mathbf{tfi} = \mathbf{tla} \Rightarrow \mathbf{Emx} = ER_k$:
we use a weakened form of (G_{52}) :

$$ER_k \leq \mathbf{Emx}; \quad (G_{52'})$$

assuming $\mathbf{tfi} = \mathbf{tla}$, we have $\mathbf{Efi} = \mathbf{Ela} = \mathbf{Emx}$ by I_{\max} , then $\mathbf{Emx} = \mathbf{Ela} \leq ER_k$ by (G_{63}) ; $\mathbf{Emx} \leq ER_k$ and $(G_{52'})$ yields $\mathbf{Emx} = ER_k$.

- $[S_6]I_{Ub1}$, that is $\forall t, t_k \leq t < \mathbf{tfi} \Rightarrow \text{Approx}(k, t) \leq \mathbf{ACR}$:
by I_{Ub1} and $t < \mathbf{tfi}$, we have $\text{Approx}(k-1, t) \leq \mathbf{ACR}$; we also have $\mathbf{tfi} \leq t_k + \tau_3$ or $t_k + \tau_3 < \mathbf{tfi}$:
- in the first case, $\text{Approx}(k, t) = \text{Approx}(k-1, t) \leq \mathbf{ACR}$ by lemma 4;
 - in the second case, $ER_k < \mathbf{Emx} \leq \mathbf{ACR}$ by (G_{52}) and contraposition of $[S_6]I_{Et1}$ shown above; taking $M = \mathbf{ACR}$ in lemma 5 yields $\text{Approx}(k, t) \leq \mathbf{ACR}$.
- $[S_6]I_{Ub2}$, that is $\forall t, \mathbf{tfi} \leq t < \mathbf{tla} \Rightarrow \text{Approx}(k, t) \leq \mathbf{Emx}$:
for t such that $\mathbf{tfi} \leq t$, we have $\text{Approx}(k-1, t) \leq \mathbf{Emx}$, by I_{Apx} ; taking $M = \mathbf{Emx}$ in lemma 5 and using (G_{52}) yields $\text{Approx}(k, t) \leq \mathbf{Emx}$.
- $[S_6]I_{Ub3}$, that is $\forall t, \mathbf{tla} \leq t \Rightarrow \text{Approx}(k, t) \leq ER_k$:
for t such that $\mathbf{tla} \leq t$, we have $\text{Approx}(k-1, t) \leq \mathbf{Ela} \leq ER_k$ by I_{Ub3} and (G_{63}) ; taking $M = ER_k$ in lemma 5 yields $\text{Approx}(k, t) \leq ER_k$.
- Note that if we can ensure $t_k + \tau_3 \leq \mathbf{tla} \leq t$, we can show $\forall t, \mathbf{tla} \leq t \Rightarrow \text{Approx}(k, t) = ER_k$.

CASE 7

if $t_k < \mathbf{tfi}$ then else if $\mathbf{ACR} \leq ER_k$ then

$$\begin{array}{l} \mathbf{Efi} := ER_k \quad || \quad \mathbf{Ela} := ER_k \quad || \quad \mathbf{Emx} := ER_k \\ \quad || \quad \mathbf{tfi} := t_k + \tau_3 \quad || \quad \mathbf{tla} := t_k + \tau_3 \end{array}$$

Let S_7 be the substitution

$$S_7 \stackrel{\text{df}}{=} T_e \quad || \quad \mathbf{Efi}, \mathbf{Ela}, \mathbf{Emx}, \mathbf{tfi}, \mathbf{tla} := ER_k, ER_k, ER_k, t_k + \tau_3, t_k + \tau_3.$$

This transition is correct if :

$$\text{Inv} \quad \wedge \quad (G_e) \quad \wedge$$

$$\mathbf{tfi} \leq t_k \quad \wedge \quad (G_{71})$$

$$\mathbf{ACR} \leq \mathbf{ER}_k \quad \Rightarrow \quad (G_{72})$$

$$[\mathbf{S}_7]\mathbf{Inv}.$$

Proof.

- $[\mathbf{S}_3]\mathbf{I}_{\mathbf{Ub}1}$, that is $\forall t, t_k \leq t < t_k + \tau_3 \Rightarrow \mathbf{Approx}(k, t) \leq \mathbf{ACR}$:
we remark that $\mathbf{utd} = \mathbf{true}$ by (G_e) and $\mathbf{tfi} \leq s$ by (G_{71}) , then $\mathbf{Approx}(k-1, t) \leq \mathbf{ACR}$ by $\mathbf{I}_{\mathbf{tfs}}$; using lemma 4 we get $\mathbf{Approx}(k, t) = \mathbf{Approx}(k-1, t)$, hence $\mathbf{Approx}(k, t) \leq \mathbf{ACR}$.
- $[\mathbf{S}_3]\mathbf{I}_{\mathbf{Ub}3}$, that is $\forall t, t_k + \tau_3 \leq t \Rightarrow \mathbf{Approx}(k, t) \leq \mathbf{ER}_k$:
we show $\forall t, t_k + \tau_3 \leq t \Rightarrow \mathbf{Approx}(k, t) = \mathbf{ER}_k$; we remark that $\mathbf{utd} = \mathbf{true}$ by (G_e) and $\mathbf{tfi} \leq s$ by (G_{71}) , then $\mathbf{Approx}(k-1, t) \leq \mathbf{ACR} \leq \mathbf{ER}_k$ by $\mathbf{I}_{\mathbf{tfs}}$; here $t_k < t$ comes from $t_k < t_k + \tau_3 \leq t$ and (G_{72}) ; using the assumption $t_k + \tau_3 \leq t$ and lemma 4, we know that $\mathbf{Approx}(k, t)$ is either equal to $\max(\mathbf{Approx}(k-1, t), \mathbf{ER}_k)$ or to \mathbf{ER}_k , that is, in both cases, to \mathbf{ER}_k .

CASE 8

if $t_k < \mathbf{tfi}$ then else if $\mathbf{ACR} \leq \mathbf{ER}_k$ then else

$$\begin{array}{l} \mathbf{Efi} := \mathbf{ER}_k \quad || \quad \mathbf{Ela} := \mathbf{ER}_k \quad || \quad \mathbf{Emx} := \mathbf{ER}_k \\ \quad || \quad \mathbf{tfi} := t_k + \tau_2 \quad || \quad \mathbf{tla} := t_k + \tau_2 \end{array}$$

Let \mathbf{S}_8 be the substitution

$$\mathbf{S}_8 \stackrel{\text{df}}{=} \mathbf{T}_e \quad || \quad \mathbf{Efi}, \mathbf{Ela}, \mathbf{Emx}, \mathbf{tfi}, \mathbf{tla} := \mathbf{ER}_k, \mathbf{ER}_k, \mathbf{ER}_k, t_k + \tau_2, t_k + \tau_2.$$

This transition is correct if :

$$\mathbf{Inv} \quad \wedge \quad (G_e) \quad \wedge$$

$$\mathbf{tfi} \leq t_k \quad \wedge \quad (G_{71})$$

$$\mathbf{ER}_k < \mathbf{ACR} \quad \Rightarrow \quad (G_{82})$$

$$[\mathbf{S}_8]\mathbf{Inv}.$$

Proof.

- $[\mathbf{S}_8]\mathbf{I}_{\mathbf{tfs}}$ and $[\mathbf{S}_8]\mathbf{I}_{\mathbf{Ub}2}$ are similar to $[\mathbf{S}_7]\mathbf{I}_{\mathbf{tfs}}$ and $[\mathbf{S}_7]\mathbf{I}_{\mathbf{Ub}2}$, replacing τ_3 with τ_2 .

- [Sg]I_{Et1}, that is $\text{ACR} < \text{ER}_k \Rightarrow t_k + \tau_2 \leq t_k + \tau_3$: the hypothesis $\text{ACR} < \text{ER}_k$ is absurd given (G_{82}).
- [Sg]I_{Ub1}, that is $\forall t, t_k \leq t < t_k + \tau_2 \Rightarrow \text{Approx}(k, t) \leq \text{ACR}$: for t such that $t_k \leq t$, we have $\text{utd} = \text{true}$ by (G_e) and $\text{tfi} \leq s$ by (G_{71}), then $\text{Approx}(k-1, t) \leq \text{ACR}$ by I_{tfs}; taking $M = \text{ACR}$ in lemma 5 and using (G_{82}) yields $\text{Approx}(k, t) \leq \text{ACR}$.
- [Sg]I_{Ub3}, that is $\forall t, t_k + \tau_2 \leq t \Rightarrow \text{Approx}(k, t) \leq \text{ER}_k$: by lemma 4, we have $\forall t, t_k + \tau_2 \leq t \Rightarrow \text{Approx}(k, t) = \text{ER}_k$.

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