# Optimizing DMA Data Transfers for Embedded Multi-Cores

# Selma Saïdi Jury members:

Oded Maler: Dir. de these

Luca Benini: Rapporteur

Eric Flamand: Examinateur

Ahmed Bouajjani: President du Jury

Albert Cohen: Rapporteur

Bruno Jego: Examinateur

#### Context of the Thesis

- Ph.D CIFRE with STMicroelectronics, supervised by,
  - Oded Maler, Verimag,
  - Bruno Jego and in collaboration with Thierry Lepley, STMicroelectronics
- Minalogic project ATHOLE
  - low-power multi-core platform for embedded systems
  - partners: ST, CEA, Thales, CWS, Verimag







### Outline

- Context and Motivation
- 2 Contribution
  - Problem Definition
  - Optimal Granularity for a Single Processor
    - 1Dim Data
    - 2Dim Data
  - Multiple Processors
  - Shared Data
- Experiments on the Cell.BE
- 4) The move towards Platform 2012
- Conclusions and Perpectives





# **Embedded Systems**

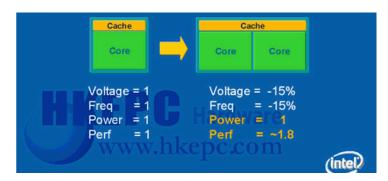
There is an increasing requirement for performance under low power constraints:

- Need to integrate more functionnalities in Embedded devices,
- Applications are becoming more computationaly intensive and power hungry,



# The Emergence of Multicore Architectures

 Running 2 processors in the same chip at half the speed will be less energy consuming and equally performant,

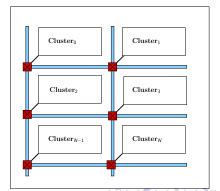






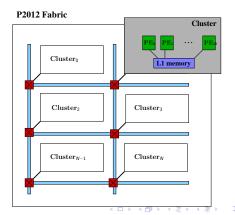
• Platform 2012: a manycore computation fabric,

#### P2012 Fabric



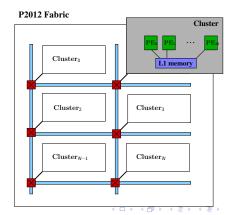


• Platform 2012: a manycore computation fabric,





- Platform 2012: a manycore computation fabric,
- main characteristic: explicitly managed Memories:

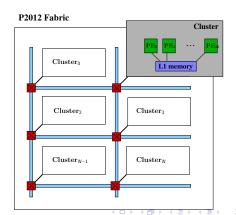




- Platform 2012: a manycore computation fabric,
- main characteristic: explicitly managed Memories:

#### Features:

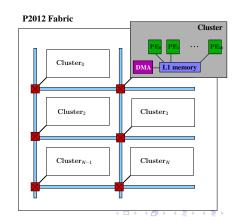
 Scratchpad memories (No caches),





- Platform 2012: a manycore computation fabric,
- main characteristic: explicitly managed Memories:

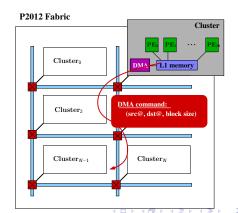
- Scratchpad memories (No caches)
- 2 DMA engine: a hardware for managing data transfers.





- Platform 2012: a manycore computation fabric,
- main characteristic: explicitly managed Memories:

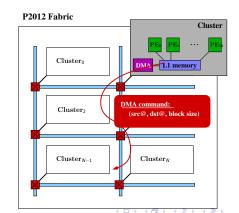
- Scratchpad memories (No caches)
- 2 DMA engine: a hardware for managing data transfers.





- Platform 2012: a manycore computation fabric,
- main characteristic: explicitly managed Memories:

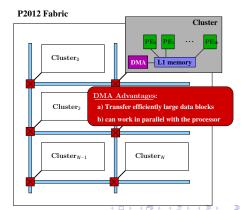
- Scratchpad memories (No caches)
- 2 DMA engine: a hardware for managing data transfers.
- data transfers are explicitly managed by the software,





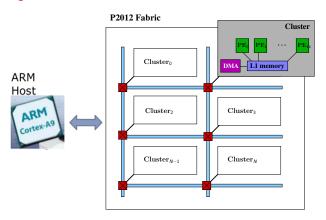
- Platform 2012: a manycore computation fabric,
- main characteristic: explicitly managed Memories:

- Scratchpad memories (No caches)
- 2 DMA engine: a hardware for managing data transfers.
- data transfers are explicitly managed by the software,





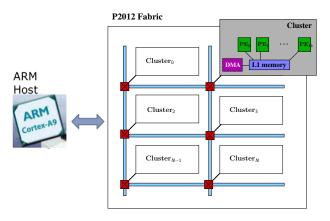
- acts as a general purpose programmable accelerator:
  - ⇒ Heterogeneous Multicore Architectures,







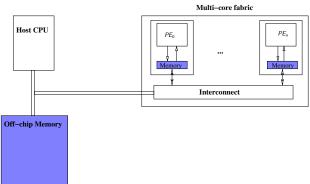
- acts as a general purpose programmable accelerator:
  - ⇒ Heterogeneous Multicore Architectures,





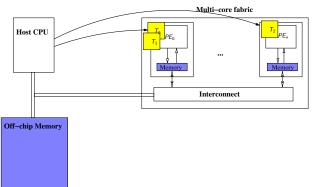
This is the class of architectures in which we are interested!!

• a powerful host processor and a multi-core fabric to accelerate computationally heavy kernels.



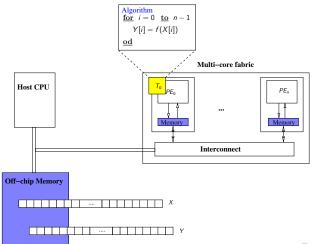


 a powerful host processor and a multi-core fabric to accelerate computationally heavy kernels.



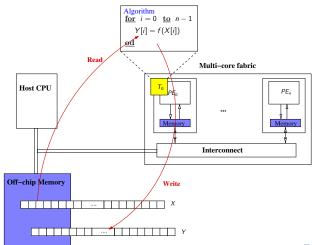


 Offloadable kernels work on large data sets, initially stored in a distant off-chip memory.





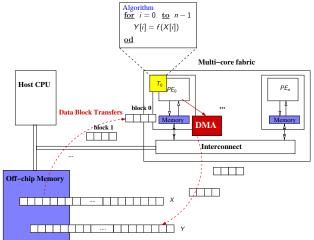
• High off-chip memory latency: accessing off-chip data is very costly







 Data is transferred to a closer but smaller on-chip memory, using DMAs (Direct Memory Access).





18

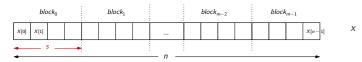
s: number of array elements in one block,

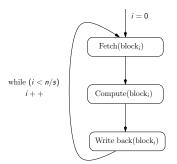
| block <sub>0</sub> |      |   |   | block <sub>1</sub> |  |  |  |   | block <sub>m-2</sub> |  |  | $block_{m-1}$ |  |  |  |        |   |
|--------------------|------|---|---|--------------------|--|--|--|---|----------------------|--|--|---------------|--|--|--|--------|---|
| X[0]               | X[1] |   |   |                    |  |  |  |   |                      |  |  |               |  |  |  | X[n-1] | X |
| •                  | _    | s | • |                    |  |  |  | n |                      |  |  |               |  |  |  | _      |   |





s: number of array elements in one block,

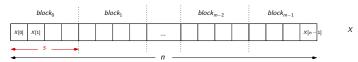


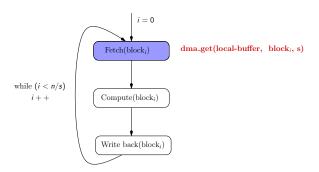






s: number of array elements in one block,

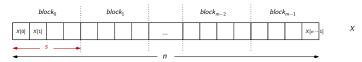


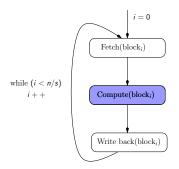






s: number of array elements in one block,

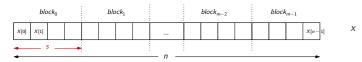


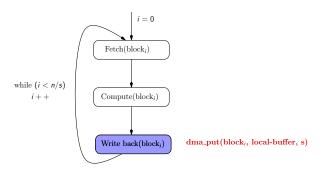






s: number of array elements in one block,



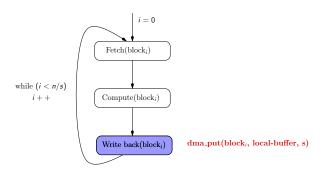






s: number of array elements in one block,

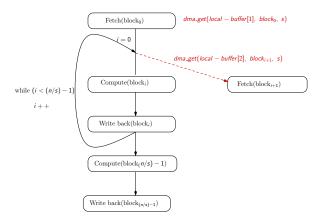






Sequential execution of computations and data transfers.

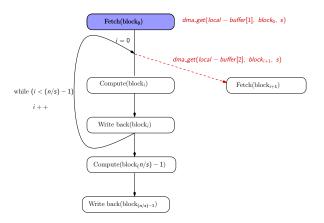
#### Asynchronous DMA calls:







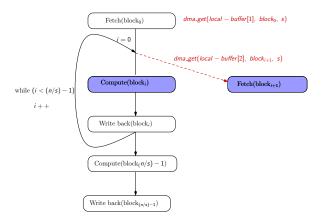
#### Asynchronous DMA calls:







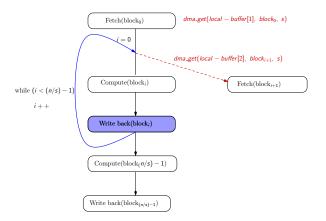
#### Asynchronous DMA calls:



Overlap of computations and data transfers.

27

#### Asynchronous DMA calls:



Overlap of computations and data transfers.

28

#### Overlap of,

- Computation of current block,
- Transfer of next block.

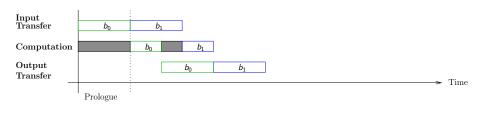






#### Overlap of,

- Computation of current block,
- Transfer of next block.



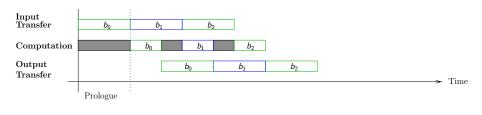






#### Overlap of,

- Computation of current block,
- Transfer of next block.





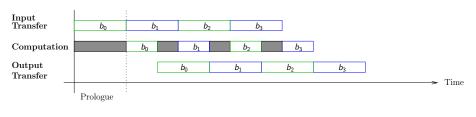


31



#### Overlap of,

- Computation of current block,
- Transfer of next block.



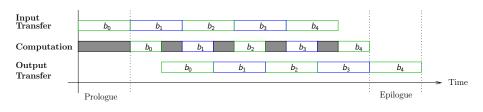






#### Overlap of,

- Computation of current block,
- Transfer of next block.



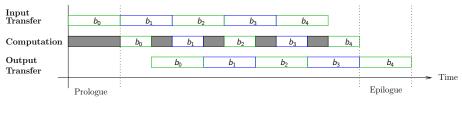






#### Overlap of,

- Computation of current block,
- Transfer of next block.



Proc idle time

Performance can be further improved by an appropriate choice of data granularity.



# **Granularity of Transfers**

1Dim Data: block size s





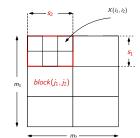


# **Granularity of Transfers**

1Dim Data: block size s



• 2Dim Data: block shape  $(s_1, s_2)$ 

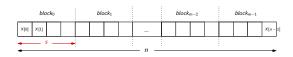




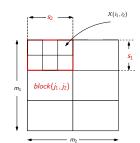


# **Granularity of Transfers**

1Dim Data: block size s



• 2Dim Data: block shape  $(s_1, s_2)$ 



#### Contribution:

We derive optimal granularity for 1D and 2D DMA transfers,

Х

### Our Contribution:

### We derive optimal granularity for 1D and 2D DMA transfers,

- 1Dim data work was published in Hipeac 2012,
   S.Saidi, P.tendulkar, T.Lepley, O.Maler, "Optimizing explicit data transfers for data parallel applications the Cell architecture"
- 2D data work was published in DSD 2012,
   S.Saidi, P.tendulkar, T.Lepley, O.Maler, "Optimal 2D Data Partitioning for DMA Transfers on MPSoCs"
  - extended version of the paper submitted to: "Embedded Hardware Design: Microprocessors and Microsystems" Journal.





- Context and Motivation
- Contribution
  - Problem Definition
  - Optimal Granularity for a Single Processor
    - 1Dim Data
    - 2Dim Data
  - Multiple Processors
  - Shared Data
- 3 Experiments on the Cell.BE
- The move towards Platform 2012
- Conclusions and Perpectives





- Context and Motivation
- Contribution
  - Problem Definition
  - Optimal Granularity for a Single Processor
    - 1Dim Data
    - 2Dim Data
  - Multiple Processors
  - Shared Data
- Experiments on the Cell.BE
- 4) The move towards Platform 2012
- 5 Conclusions and Perpectives





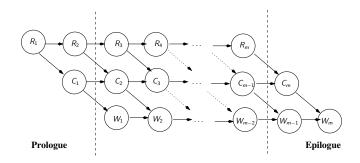
- Context and Motivation
- Contribution
  - Problem Definition
  - Optimal Granularity for a Single Processor
    - 1Dim Data
      - 2Dim Data
  - Multiple Processors
  - Shared Data
- Experiments on the Cell.BE
- 4) The move towards Platform 2012
- 5 Conclusions and Perpectives





# Software Pipelining

• We want to optimize execution of the pipeline.



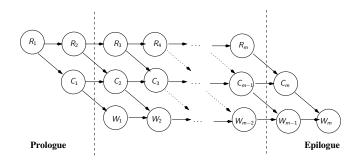




24 October 2012

# Software Pipelining

• We want to optimize execution of the pipeline.



### Optimal Granularity:

What is the Granularity choice that optimizes performance?



# Computation Regime and Transfer Regime

• T and C: Transfer and Computation time of a block

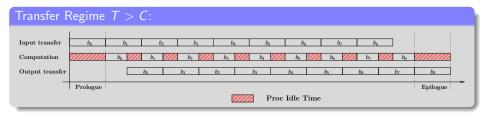


Computation Regime C > T:



# Computation Regime and Transfer Regime

• T and C: Transfer and Computation time of a block





## In the Computation Regime:









## Optimal Granularity: Problem Formulation

#### 1Dim Data: block size

Find s\* such that,

Min T(s) s.t.

$$T(s) \leq C(s)$$
  
 $(s) \in [1..n]$   
 $s < M$ 

### 2Dim Data: block shape





### Optimal Granularity: Problem Formulation

#### 1Dim Data: block size

Find s\* such that,

Min T(s) s.t.

$$T(s) \le C(s)$$
$$(s) \in [1..n]$$
$$s < M$$

### 2Dim Data: block shape

Find  $(s_1^*, s_2^*)$  such that,

Min  $T(s_1, s_2)$  s.t.

$$T(s_1, s_2) \le C(s_1, s_2)$$
  
 $(s_1, s_2) \in [1...n_1] \times [1...n_2]$   
 $s_1 \times s_2 \le M$ 



43



- Context and Motivation
- Contribution
  - Problem Definition
  - Optimal Granularity for a Single Processor
    - 1Dim Data
    - 2Dim Data
  - Multiple Processors
  - Shared Data
- Experiments on the Cell.BE
- The move towards Platform 2012
- Conclusions and Perpectives





- Context and Motivation
- Contribution
  - Problem Definition
  - Optimal Granularity for a Single Processor
    - 1Dim Data
      - 2Dim Data
  - Multiple Processors
  - Shared Data
- Experiments on the Cell.BE
- The move towards Platform 2012
- Conclusions and Perpectives





Characterization of Computation and Transfer Time:

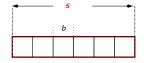




24 October 2012

#### Characterization of Computation and Transfer Time:

• s: nb array elements clustered in one (Contiguous) block,

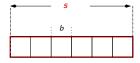


Computation time C(s):



#### Characterization of Computation and Transfer Time:

• s: nb array elements clustered in one (Contiguous) block,



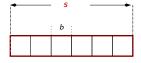
### Computation time C(s):

•  $\omega$ : time to compute one element,



#### Characterization of Computation and Transfer Time:

• s: nb array elements clustered in one (Contiguous) block,



### Computation time C(s):

•  $\omega$ : time to compute one element,

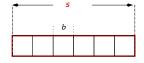
$$C(s) = \omega \cdot s$$





#### Characterization of Computation and Transfer Time:

• s: nb array elements clustered in one (Contiguous) block,



### Computation time C(s):

•  $\omega$ : time to compute one element,

$$C(s) = \omega \cdot s$$

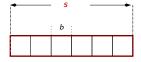
### DMA Transfer time T(s):

• 1: fixed DMA initialization cost,



#### Characterization of Computation and Transfer Time:

• s: nb array elements clustered in one (Contiguous) block,



### Computation time C(s):

•  $\omega$ : time to compute one element,

$$C(s) = \omega \cdot s$$

### DMA Transfer time T(s):

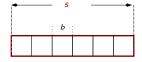
- I: fixed DMA initialization cost.
- $\bullet$   $\alpha$ : transfer cost per byte,



Selma Saidi

#### Characterization of Computation and Transfer Time:

• s: nb array elements clustered in one (Contiguous) block,



### Computation time C(s):

•  $\omega$ : time to compute one element,

$$C(s) = \omega \cdot s$$

### DMA Transfer time T(s):

• *I*: fixed DMA initialization cost,

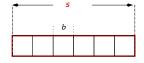
•  $\alpha$ : transfer cost per byte,

• b: size of one array element,



### Characterization of Computation and Transfer Time:

• s: nb array elements clustered in one (Contiguous) block,



### Computation time C(s):

•  $\omega$ : time to compute one element,

$$C(s) = \omega \cdot s$$

### DMA Transfer time T(s):

- 1: fixed DMA initialization cost,
- $\bullet$   $\alpha$ : transfer cost per byte,
- b: size of one array element,

$$T(s) = I + \alpha \cdot b \cdot s$$



Selma Saidi

#### Pb Formulation

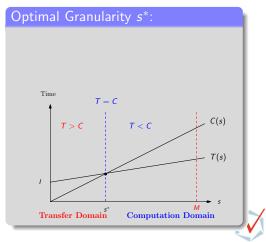
Min T(s) s.t.

$$T(s) \le C(s)$$

$$s \in [1..n]$$

$$s \le M$$

- s: block size
- M: Memory limitation



40.40.45.45. 5 90/

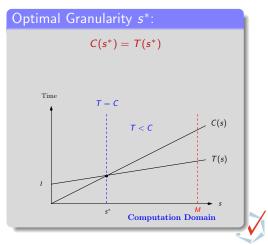
47

#### Pb Formulation

Min T(s) s.t.

$$T(s) \le C(s)$$
  
 $s \in [1..n]$   
 $s \le M$ 

- s: block size
- M: Memory limitation



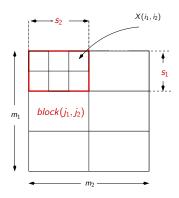
- Context and Motivation
- Contribution
  - Problem Definition
  - Optimal Granularity for a Single Processor
    - 1Dim Data
    - 2Dim Data
  - Multiple Processors
  - Shared Data
- Experiments on the Cell.BE
- The move towards Platform 2012
- Conclusions and Perpectives





#### Characterization of Computation and Transfer Time:

•  $(s_1 \times s_2)$ : nb array elements clustered in one (square) block,



Computation time  $C(s_1, s_2)$ :

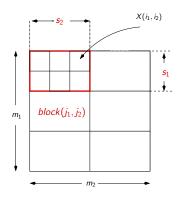


50



#### Characterization of Computation and Transfer Time:

•  $(s_1 \times s_2)$ : nb array elements clustered in one (square) block,



### Computation time $C(s_1, s_2)$ :

•  $\omega$ : time to compute one element,

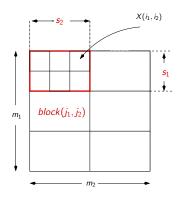




24 October 2012

#### Characterization of Computation and Transfer Time:

•  $(s_1 \times s_2)$ : nb array elements clustered in one (square) block,



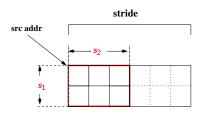
### Computation time $C(s_1, s_2)$ :

•  $\omega$ : time to compute one element,

$$C(s_1, s_2) = \omega \cdot s_1 \cdot s_2$$







### Strided DMA Transfer time $T(s_1, s_2)$ :

• I1: transfer cost overhead per line,

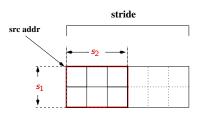
$$T(s_1, s_2) = I + I_1 s_1 + \alpha \cdot b \cdot s_1 \cdot s_2$$



51



Selma Saidi



### Strided DMA Transfer time $T(s_1, s_2)$ :

• I1: transfer cost overhead per line,

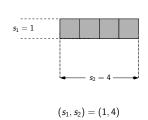
$$T(s_1, s_2) = I + I_1 s_1 + \alpha \cdot b \cdot s_1 \cdot s_2$$

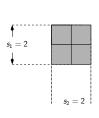
Strided DMA transfers are costlier than contiguous transfers

Selma Saidi

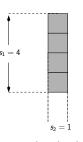
# Influence of the Block Shape on the DMA Transfer Cost

Different block shapes with same area BUT different DMA transfer time,









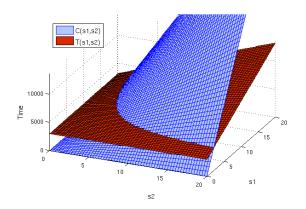
$$(s_1, s_2) = (4, 1)$$





•  $C(s_1, s_2)$ : computation time of a block,

•  $T(s_1, s_2)$ : transfer time of a block,

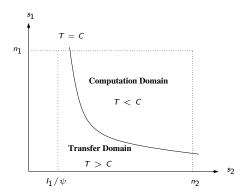




53



- $C(s_1, s_2)$ : computation time of a block,
- $T(s_1, s_2)$ : transfer time of a block,





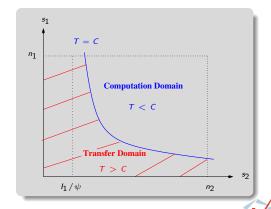


#### Pb Formulation

Min  $T(s_1, s_2)$  s.t.

$$T(s_1, s_2) \le C(s_1, s_2)$$
  
 $(s_1, s_2) \in [1...n_1] \times [1...n_2]$   
 $s_1 \times s_2 \le M$ 

- s<sub>1</sub>: block height
- s2: block width



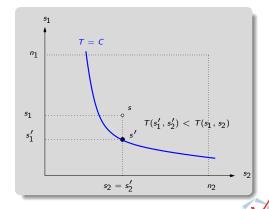


#### Pb Formulation

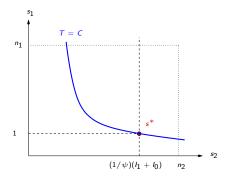
Min  $T(s_1, s_2)$  s.t.

$$T(s_1, s_2) \le C(s_1, s_2)$$
  
 $(s_1, s_2) \in [1..n_1] \times [1..n_2]$   
 $s_1 \times s_2 \le M$ 

- s<sub>1</sub>: block height
- s2: block width







$$\begin{cases} \text{ Block Height}: s_1^* = 1 \\ \text{ Block Width}: s_2^* = (1/\psi)(I_1 + I_0) \end{cases}$$

Optimal granularity is the Contiguous block to reach the computation regime:

Optimizing DMA Transfers

57

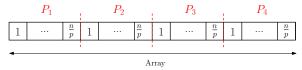
### Outline

- Context and Motivation
- Contribution
  - Problem Definition
  - Optimal Granularity for a Single Processor
    - 1Dim Data
      - 2Dim Data
  - Multiple Processors
  - Shared Data
- 3 Experiments on the Cell.BE
- The move towards Platform 2012
- 5 Conclusions and Perpectives





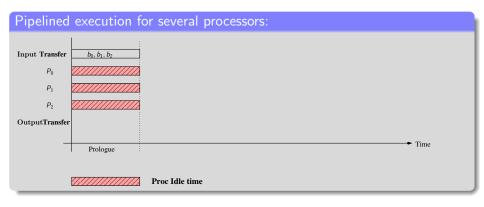
• Partitioning: *p* contiguous chunks of data



 Processors are identical: same local store capacity, same double buffering granularity...etc.



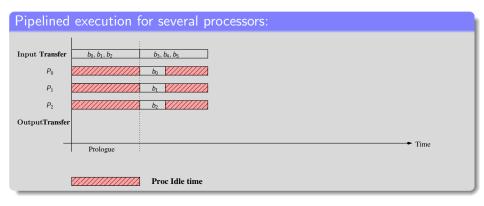




processors DMA requests are done concurrently,





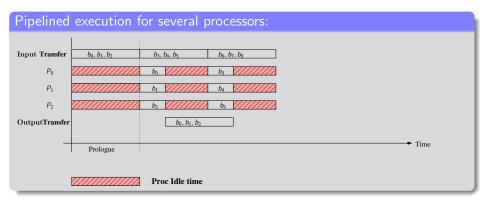


processors DMA requests are done concurrently,





24 October 2012

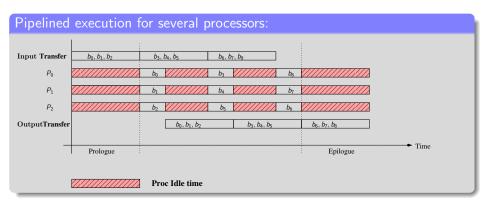


processors DMA requests are done concurrently,



60

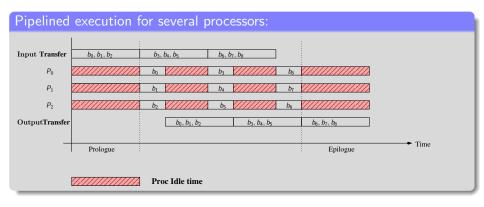




processors DMA requests are done concurrently,







processors DMA requests are done concurrently,

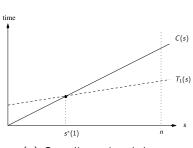
$$T(s,p) = I + \alpha(p) \cdot b \cdot s$$

 $\alpha(p)$ : transfer cost per byte given contentions of p concurrent transfer requests.

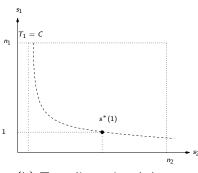
60

# Multiple Processors: Optimal Granularity

• Optimal granularity given p processors:  $s^*(p)$ ,



(a) One-dimensional data



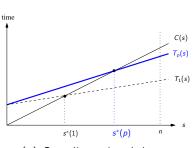
(b) Two-dimensional data



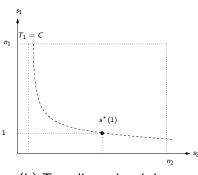


# Multiple Processors: Optimal Granularity

• Optimal granularity given p processors:  $s^*(p)$ ,



(a) One-dimensional data



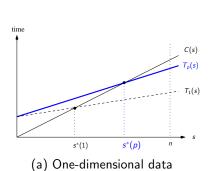
(b) Two-dimensional data

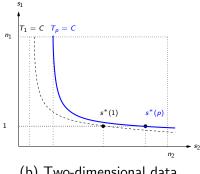




# Multiple Processors: Optimal Granularity

• Optimal granularity given p processors:  $s^*(p)$ ,





(b) Two-dimensional data

Optimal Granularity increases with number of processors



## Summary:

We derived optimal granularity,

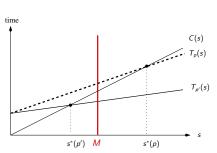
- main idea: balance between computation and transfer time of a block,
- 2D data: block shape influences transfer time (overhead per line,  $I_1$ )
- multiple processors: number of processors influence transfer time (with  $\alpha(p)$ )



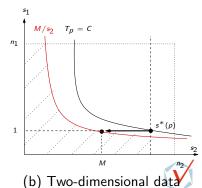


# **Local Memory Constraint**

- What if Optimal Granularity does not fit in Local memory?
  - 1 take available memory space,
  - 2 reduce the number of processors,



(a) One-dimensional data



### Outline

- Context and Motivation
- Contribution
  - Problem Definition
  - Optimal Granularity for a Single Processor
    - 1Dim Data
    - 2Dim Data
  - Multiple Processors
  - Shared Data
- Experiments on the Cell.BE
- The move towards Platform 2012
- Conclusions and Perpectives





## Applications with shared data: 1Dim

• Data parallel loop with shared input data:

for 
$$i := 0$$
 to  $n-1$  do  $Y[i] := f(X[i], V[i]);$   $V[i] = \{X[i-1], X[i-2], ..., X[i-k]\}$  od



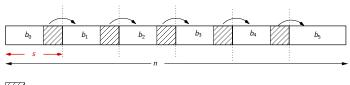


## Applications with shared data: 1Dim

• Data parallel loop with shared input data:

for 
$$i := 0$$
 to  $n-1$  do  $Y[i] := f(X[i], V[i]);$   $V[i] = \{X[i-1], X[i-2], ..., X[i-k]\}$  od

Neighboring blocks share data:





Neighboring Shared Data



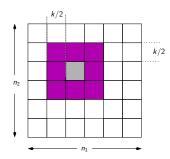


### Shared Data: 2Dim

Data parallel loop with shared input data:

$$\begin{array}{l} \text{for } i:=1 \text{ to } n_1 \text{ do} \\ \text{ for } i:=2 \text{ to } n_2 \text{ do} \\ Y[i_1,i_2]:=f(X[i_1,i_2], \textcolor{red}{V[i_1,i_2]}); \\ V[i_1,i_2]=\{X[i_1-1,i_2], X[i_1,i_2-1], \\ ..., X[i_1-k,i_2]\} \end{array}$$
 od

symmetric window,







24 October 2012

Compare strategies for transferring shared data:

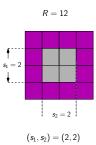
- Replication: via DMA transfers from the off-chip memory to local memory.
- Inter-processor communication: processors exchange data via the network-on-chip between the cores;
- Solution
  Local buffering: via local copies done by the processors.

Based on a parametric study, we derive optimal strategy and granularity for transferring shared data,





- we consider Replication for transferring shared data,
- R: size of replicated data.



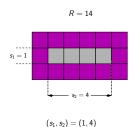




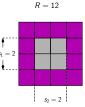
### Influence of the block shape on the size of share data:

• Compare Transfer cost of a flat and a square block,

R: size of replicated data.



$$s_2 = 2$$
 $(s_1, s_2) = (2, 2)$ 



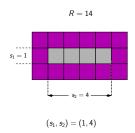


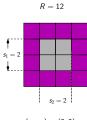


### Influence of the block shape on the size of share data:

Compare Transfer cost of a flat and a square block,

R: size of replicated data.





$$(s_1, s_2) = (2, 2)$$

More Replicated dataOverhead

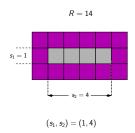


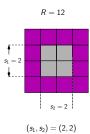


### Influence of the block shape on the size of share data:

Compare Transfer cost of a flat and a square block,

R: size of replicated data.





More Replicated data
 Overhead

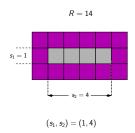
More transfer linesOverhead

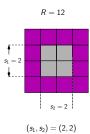


### Influence of the block shape on the size of share data:

Compare Transfer cost of a flat and a square block,

R: size of replicated data.





More Replicated data
 Overhead

More transfer linesOverhead



#### Problem Formulation

Find  $(s_1^*, s_2^*)$  such that,

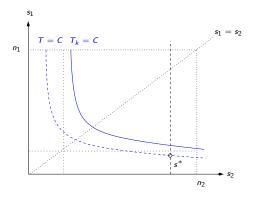
min 
$$T(s_1 + k, s_2 + k)$$
 s.t.

$$T(s_1 + k, s_2 + k) \le C(s_1, s_2)$$
  
 $(s_1, s_2) \in [1..n_1] \times [1..n_2]$   
 $(s_1 + k) \times (s_2 + k) \le M$ 





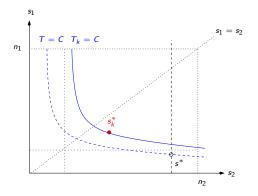
24 October 2012







Optimal shape: between a square and a flat shape,



$$\begin{cases} s_1^* = \Delta + (c_1/\psi)(1/D) \\ s_2^* = \Delta + (I_1/\psi)(1+D) \end{cases}$$





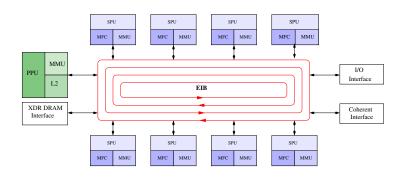
### Outline

- Context and Motivation
- Contribution
  - Problem Definition
  - Optimal Granularity for a Single Processor
    - 1Dim Data
    - 2Dim Data
  - Multiple Processors
  - Shared Data
- 3 Experiments on the Cell.BE
- The move towards Platform 2012
- Conclusions and Perpectives





### Overview of Cell B.E. Architecture

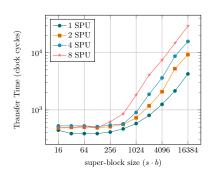


#### Platform Characteristics:

- 9-core heterogeneous multi-core architecture, with a Power Processor Element(PPE) and 8 Synergistic Processing Elements(SPE).
- Limited local store capacity per SPE: 256 Kbytes
- Explicitely managed memory system, using DMAs

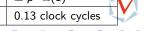


## Measured DMA Latency



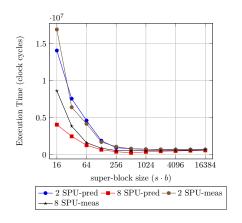
### • Profiled hardware parameters:

| DMA issue time                              | 1            | ≤ 400 clock cycles         |
|---|--------------|----------------------------|
| Off-chip memory transfer cost/byte: 1 proc  | $\alpha$ (1) | 0.22 clock cycles          |
| Off-chip memory transfer cost/byte: p procs |              | $\simeq p \cdot \alpha(1)$ |
| inter-processor comm transfer cost/byte for | β            | 0.13 clock cycles          |



75

# Optimal Granularity: 1Dim Data, No Sharing

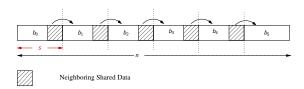


• predicted optimal granularities give good performance.

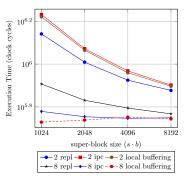




# Optimal Granularity: 1Dim Data, Sharing



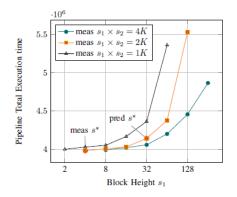
#### Comparing several strategies:





# Optimal Granularity: 2Dim Data, Sharing

• We implement double buffering on a mean filtering algorithm,





• predicted optimal granularities give good performance.

### Outline

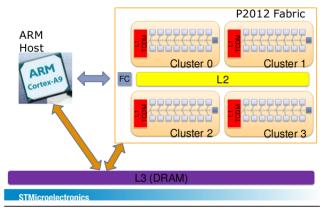
- Context and Motivation
- Contribution
  - Problem Definition
  - Optimal Granularity for a Single Processor
    - 1Dim Data
    - 2Dim Data
  - Multiple Processors
  - Shared Data
- Experiments on the Cell.BE
- The move towards Platform 2012
- 5 Conclusions and Perpectives





# P2012 Memory Hierarchy

- Intra cluster L1 memory (256 Kbytes),
- Inter cluster L2 memory,
- Off-chip L3 memory





24 October 2012

## **DMA Latency**

we measure the DMA latency on P2012,

10<sup>4</sup> 1 PE 2 PE 4 PE 8 PE 16 PE 16 PE 1024 4096 super-block size (s · b)

DMA performance Model:

$$T(s,p) = I + \alpha(p)bs$$

$$I = 240$$
 cycles

| p  | $\alpha(p)$ |
|----|-------------|
| 1  | 0.25        |
| 2  | 0.45        |
| 4  | 0.65        |
| 8  | 1.15        |
| 16 | 2.15        |





## **DMA Latency**

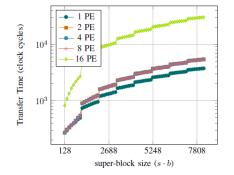
we measure the DMA latency on P2012,

DMA performance Model:

$$T(s,p) = I + \alpha(p)bs$$

$$I = 240$$
 cycles

| p  | $\alpha(p)$ |
|----|-------------|
| 1  | 0.25        |
| 2  | 0.45        |
| 4  | 0.65        |
| 8  | 1.15        |
| 16 | 2.15        |







### DMA Transfers in a Cluster

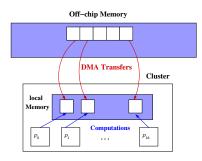
Shared Local Memory and shared DMA:

- 2 approaches for transferring data,
  - Liberal: Processors fetch data independently
  - Collaborative: Processors fetch data collectively





### Liberal Approach: Processors fetch data independently



```
T(s, p) = I + \alpha(p)bs

C(s, p) = o + \omega s

s \le M/p
```

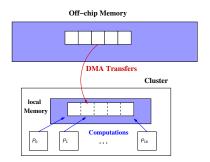
```
Program 1 (Liberal): Kernel( data_type global *GBuffer, param1, param2 ....) {
data_type LBuffer [size];
...
async_work_item_copy(GBuffer, LBuffer, size, e);
...
wait_event(e);
```

Opencl Kernel: work item = processor async\_work\_item\_copy: DMA fetch for each processor



24 October 2012

### Collab Approach: Processors fetch data Collectively



$$T(s,p) = I + \alpha(1)bs$$

$$C(s,p) = o(p) + (\omega/p)s$$

$$s < M$$

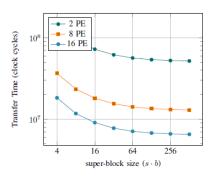
```
Program 2 (Collaborative): Kernel (data_type global* GBuffer, data_type local *LBuffer, param1, param2 ....) {
...
async_work_group_copy(GBuffer, LBuffer, size, e);
...
wait_group_event(e);
```

Opencl Kernel: work group = cluster async\_work\_group\_copy: DMA fetch for the cluster



### Liberal Approach: Processors fetch data independently

- ⊖ increase of number of processors reduces max buffer size,
  - double buffering implementation results:
     Optimal Granularity does not fit in the available memory space

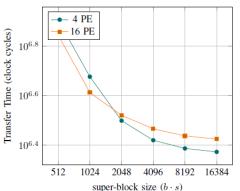






### Liberal Approach: Processors fetch data independently

- - double buffering implementation results: Performance degradation when increase number of processors







## On-going Work:

- find the right balance between number of processors and Memory space budget,
- compare both liberal and collaborative approach,





### Outline

- Context and Motivation
- Contribution
  - Problem Definition
  - Optimal Granularity for a Single Processor
    - 1Dim Data
    - 2Dim Data
  - Multiple Processors
  - Shared Data
- Experiments on the Cell.BE
- The move towards Platform 2012
- Conclusions and Perpectives





### Conclusion

- We presented a general methodology for automating decisions about Optimal granularity for data transfers,
- we capture the following facts,
  - Block shape and size influence the transfer/computation Ratio,
  - 2 DMA performance (sensitivity to the block shapes, number of processors)
  - 3 tradeoff between Strided DMA overhead vs size of replicated data





### Perspectives

- Consider other applications patterns,
- Capture variations (hardware and Software),
- Generalize the approach to more than 2 memory levels,
- Integrate this work in a complete compilation flow,
- ombine task and data parallelism,
- **6** ...





Thank You !!!



