A SILENT SELF-STABILIZING ALGORITHM FOR FINDING CUT-NODES AND BRIDGES

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ABSTRACT
In this paper, we present a self-stabilizing algorithm for finding cut-nodes and bridges in arbitrary rooted networks with a low memory requirement ($O(\log(n))$ bits per processor where $n$ is the number of processors). Our algorithm is silent and must be composed with a silent self-stabilizing algorithm computing a Depth-First Search (DFS) Spanning Tree of the network. So, in the paper, we will prove that the composition of our algorithm with any silent self-stabilizing DFS algorithm is self-stabilizing. Finally, we will show that our algorithm needs $O(n^2)$ moves to reach a terminal configuration once the DFS spanning tree is computed. Note that this time complexity is equivalent to the best proposed solutions.

Keywords: Distributed systems, self-stabilizing algorithms, undirected graphs, cut-node, bridge.

1. Introduction
Consider a connected undirected graph $G = (V, E)$ where $V$ is the set of $n$ nodes and $E$ is the set of $m$ edges. A node $p \in V$ is a cut-node (or an articulation point) of $G$ if the removal of $p$ disconnects $G$. In the same way, an edge $(p, q) \in E$ is a bridge if the removal of $(p, q)$ disconnects $G$. When the graph represents a communication network then the existence of cut-nodes or bridges can become the potential cause for congestion in the network. Thus, from the fault tolerance point of view, the identification of cut-nodes and bridges of a network is crucial.

In this paper, we are interested in finding cut-nodes and bridges in distributed systems. Another desirable property for a distributed system is to withstand transient failures. The concept of self-stabilization [1] is the most general technique to design a system to tolerate arbitrary transient faults. A self-stabilizing system, regardless of the initial states of the processors and messages initially in the links, is guaranteed to converge to the intended behavior in finite time. In such systems, a silent algorithm is an algorithm which, from any arbitrary initial configuration, reaches, in a finite number of steps, a terminal configuration where no processor can execute any action; this configuration must satisfy some properties for all possible executions.

1.1. Related Work
Some algorithms for finding cut-nodes and bridges have been proposed in the graph
theory, e.g., by Paton [2] and Tarjan [3], the latter has a linear time complexity. This problem has also been investigated in the context of parallel and distributed computing [4,5,6]. In self-stabilizing systems, Chaudhuri and Karaata present silent algorithms in [7,8,9,10] for finding cut-nodes and bridges. All these solutions work with an underlying spanning tree construction algorithm. The solutions proposed in [9,10] use a DFS spanning tree of the network while a BFS spanning tree is required in [7,8]. Algorithms from [9,10] offer the best time complexity: they need $O(n^2)$ moves to stabilize once the DFS spanning tree is computed (instead of $O(n^2 \times m)$ moves for [7,8]). However, for all these solutions, the memory requirement is $\Omega(m \times \log(m))$ bits per processor (without taking account of variables used for the spanning tree computing). Indeed, in these algorithms, every processor must locally maintain a set of edges.

1.2. Contributions

In this paper, we present a new silent self-stabilizing distributed algorithm for finding cut-nodes and bridges. This algorithm must be composed with a silent self-stabilizing algorithm computing a DFS spanning tree of the network. Until now, our protocol is the best algorithm solving this problem in term of memory requirement, i.e., $O(\log(n))$ bits per processor (without taking account of variables used for the spanning tree computing). Moreover, once the DFS spanning tree is computed, our algorithm reaches a terminal configuration in $O(r^2)$ moves and $O(H)$ rounds where $H$ is the height of the spanning tree. This time complexity corresponds to the best proposed solutions.

1.3. Outline of the paper

In the next section (Section 2), we describe the distributed system and the model in which our protocol is written. We present and prove our solution in Sections 3 and 4. In Section 5, we discuss about some complexity results. Finally, we make concluding remarks in Section 6.

2. Preliminaries

2.1. Distributed System

We consider a distributed system as an undirected connected graph $G = (V, E)$ where $V$ is a set of processors ($|V| = n$) and $E$ is the set of bidirectional communication links. We consider networks which are asynchronous and rooted, i.e., all processors, except the root, are anonymous. We denote the root processor by $r$. A communication link between two processors $p$ and $q$ will be denoted by $(p, q)$. Every processor $p$ can distinguish all its links. To simplify the presentation, we refer to a link $(p, q)$ of a processor $p$ by the label $q$. We assume that the labels of $p$ are stored in the set $N(e_{q})$. We assume that $N(e_{q})$ is a constant ($N(e_{q})$ is shown as an input from the system).

2.2. Computational Model

In the computation model that we use each processor executes the same program except

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*a*Breath-First Search.

*b*Every variable or constant $X$ of a processor $p$ will be noted $X_p$. 

r. We consider the local shared memory model of communication. The program of every processor consists of a set of shared variables (henceforth, referred to as variables) and a finite set of actions. A processor can only write to its own variables, and read its own variables and variables owned by the neighboring processors. Each action is of the following form:

\[ <\text{label}> : : <\text{guard}> \rightarrow <\text{statement}>. \]

The guard of an action in the program of \( p \) is a boolean expression involving the variables of \( p \) and its neighbors. The statement of an action of \( p \) updates one or more variables of \( p \). An action can be executed only if its guard is satisfied. We assume that the actions are atomically executed, meaning, the evaluation of a guard and the execution of the corresponding statement of an action, if executed, are done in one atomic step.

The state of a processor is defined by the value of its variables. The state of a system is the product of the states of all processors (\( \in V \)). We will refer to the state of a processor and system as a (local) state and (global) configuration, respectively. Let \( C \), the set of all possible configurations of the system. An action \( A \) is said to be enabled in \( \gamma \in C \) at \( p \) if the guard of \( A \) is true at \( p \) in \( \gamma \). A processor \( p \) is said to be enabled in \( \gamma \) (\( \gamma \in C \)) if there exists an enabled action \( A \) in the program of \( p \) in \( \gamma \).

Let a distributed protocol \( P \) be a collection of binary transition relations denoted by \( \rightarrow \), on \( C \). A computation of a protocol \( P \) is a maximal sequence of configurations \( e = (\gamma_0, \gamma_1, \ldots, \gamma_i, \gamma_{i+1}, \ldots) \), such that for \( i \geq 0, \gamma_i \rightarrow \gamma_{i+1} \) (called a single computation step or move) if \( \gamma_{i+1} \) exists, else \( \gamma_i \) is a terminal configuration. Maximal means that the sequence is either finite (and no action of \( P \) is enabled in the terminal configuration) or infinite. All computations considered in this paper are assumed to be maximal. The set of all possible computations of \( P \) in the system \( S \) is denoted as \( \mathcal{E} \).

In a step of computation, first, all processors check the guards of their actions. Then, some enabled processors are chosen by a daemon. Finally, the “elected” processors execute one or more of their enabled actions. There exists several kinds of daemon. Here, we use a central daemon, i.e., during a computation step, if one or more processors are enabled, the daemon chooses one of these enabled processors to execute an action. Furthermore, we assume that the daemon is unfair, i.e., it can forever prevent a processor to execute an action except if it is the only enabled processor. The unfair daemon is the weakest scheduling assumption.

We consider that any processor \( p \) executed a disable action in the computation step \( \gamma_i \rightarrow \gamma_{i+1} \) if \( p \) was enabled in \( \gamma_i \) and not enabled in \( \gamma_{i+1} \), but did not execute any action between these two configurations (the disable action represents the following situation: at least one neighbor of \( p \) changes its state between \( \gamma_i \) and \( \gamma_{i+1} \), and this change effectively made the guard of all actions of \( p \) false).

In order to compute the time complexity, we use the definition of round [12]. This definition captures the execution rate of the slowest processor in any computation. Given a computation \( e \) (\( e \in \mathcal{E} \)), the first round of \( e \) (let us call it \( e' \)) is the minimal prefix of \( e \) containing the execution of one action (an action of the protocol or the disable action) of every enabled processor in the first configuration. Let \( e'' \) be the suffix of \( e \) such that \( e = e'e'' \). Then second round of \( e \) is the first round of \( e'' \), and so on.

2.3. Self-Stabilizing System
Let \( PR \) be a predicate defined over \( C \). The protocol \( P \) running on the distributed system \( S \) is said to be self-stabilizing with respect to \( PR \) if it satisfies:

- **Closure**: If a configuration \( \gamma \) satisfies \( PR \), then any configuration that is reachable from \( \gamma \) using \( P \) also satisfies \( PR \).

- **Convergence**: Starting from an arbitrary configuration, the distributed system \( S \) is guaranteed to reach a configuration satisfying \( PR \) in a finite number of steps of \( P \).

Configurations satisfying \( PR \) are said to be legitimate. Similarly, a configuration that does not satisfy \( PR \) is referred to as an illegitimate state. To show that an algorithm is self-stabilizing with respect to \( PR \), we need to show the satisfiability of both closure and convergence conditions. In the case of silent algorithms, we have just to show the convergence to a terminal configuration that satisfies \( PR \). After, because this configuration is terminal, the closure is trivially satisfied.

### 2.4. Definitions and Notations

**Definition 1 (Path)** The sequence of nodes \( p_1, ..., p_k \) is a path of \( G \) if and only if \( \forall i \in \{1, ..., k-1\}, (p_i, p_{i+1}) \in E \) (the set of edges of \( G \)).

**Definition 2 (Length of a Path)** The length of a path \( P \), noted \( \text{length}(P) \), is the number of edges which compose \( P \).

**Definition 3 (Connected Graph)** An undirected graph \( GC \) is connected if and only if, for each pair of distinct nodes \( (p, q) \), there exists a path in \( GC \) between \( p \) and \( q \).

**Definition 4 (Partial Graph)** The graph \( GA = (V, A) \) is a partial graph of \( G = (V, E) \) if and only if \( A \subseteq E \).

**Definition 5 (Subgraph)** The subgraph of \( G = (V, E) \) induced by \( S \) \( (S \subseteq V) \) is the graph \( G_S = (S, E_S) \) such that \( E_S = E \cap S^2 \).

**Definition 6 (Spanning Tree)** A graph \( T = (V, E_T) \) is a spanning tree of \( G \) if and only if \( T \) is a partial connected graph of \( G \) where \( |E_T| = n - 1 \).

In a rooted spanning tree \( T(r) = (V, E') \), we distinguish a node \( r \) called root. We define the height of a node \( p \) in \( T(r) \) (noted \( h(p) \)) as the length of the simple (loopless) path from \( r \) to \( p \) in \( T(r) \). \( H = \max_{p \in T(r)} \{h(p)\} \) represents the height of \( T(r) \). For a node \( p \neq r \), a node \( q \in V \) is said to be the parent of \( p \) in \( T(r) \), noted \( \text{Parent}(p) \), if and only if \( q \) is the neighbor of \( p \) such that \( h(p) = h(q) + 1 \), conversely, \( p \) is said to be the child of \( q \) in \( T(r) \). \( \text{Children}(p) \) denotes the set of children of a node \( p \) in \( T(r) \). A node \( p_1 \) is said to be an ancestor of another node \( p_k \) in \( T(r) \) (with \( k > 1 \)) if there exists a sequence of nodes \( p_1, ..., p_k \) such that \( \forall j \), with \( 1 \leq j < k \), \( p_j \) is the parent of \( p_{j+1} \) in \( T(r) \), conversely \( p_k \) is said to be a descendant of \( p_1 \). We note \( T(p) \) the subtree of \( T(r) \) rooted at \( p \) \((p \in V)\), i.e., the subgraph of \( T(r) \) induced by \( p \) and its descendants in \( T(r) \). We call tree edges, the edges of \( E' \) and non-tree edges, the edges of \( E \setminus E' \). A non-tree neighbor of \( p \) is a node linked to \( p \) by a
Definition 7 (DFS Spanning Tree) \( T(r) \) is a DFS spanning tree of \( G \) if and only if \( T(r) \) is a spanning tree of \( G \) and \( \forall (p, q) \in V^2 \) if \( p \) is a neighbor of \( q \) in \( G \) then \( p \) is either an ancestor or a descendant of \( q \) in \( T(r) \).

From Definition 7, we can deduce this useful property about the DFS spanning trees.

Property 1 Let \( p \in V \). Let \( T(r) \) be a DFS Spanning Tree of \( G \) (rooted at \( r \)). \( \forall q \in T(p) \), every non-tree neighbor of \( q \) is either an ancestor or a descendant of \( p \) in \( T(r) \).

Now, we give the formal definitions of cut-nodes and bridges.

Definition 8 (Cut-node) A node \( p \in V \) is a cut-node (or an articulation point) of \( G \) if and only if the subgraph of \( G \) induced by \( V \setminus \{p\} \) is disconnected.

Definition 9 (Bridge) An edge \( (p, q) \in E \) is a bridge of \( G \) if and only if the partial graph \( G' = (V, E \setminus \{(p,q)\}) \) is disconnected.

3. Algorithm

In this section, we present a silent self-stabilizing algorithm called Algorithm 3. Algorithms 1 and 2 formally describe Algorithm 3. Our approach (Subsection 3.1) is mainly based on two results of Tarjan. These results concern the DFS spanning trees and their properties. Algorithm 3 is informally describe in Subsection 3.3. This algorithm assumes that a DFS spanning tree of \( G \) (rooted at \( r \)) is available. We note \( T(r) = (V, E') \) this tree. \( T(r) \) can be obtained by applying any silent self-stabilizing DFS algorithm. Thus, in Subsection 3.2, we also discuss about the different silent self-stabilizing DFS algorithms of the literature. Finally, Algorithm 3 runs concurrently with any silent self-stabilizing DFS algorithm following the collateral composition rules (as defined in [13]). So, in Subsection 3.4, we also recall the definition of this composition. In addition, we propose a general scheme for proving the correctness of a such composite algorithm (Theorem 3).

3.1. Approach

To implement our algorithm, we use two theorems established by Tarjan in [3]:

Theorem 1 \( r \) (root of \( G \)) is a cut-node if and only if \( |\text{Children}(r)| \geq 2 \).

Theorem 2 \( \forall p \in V \setminus \{r\}, \) \( p \) is a cut-node if and only if there exists a node \( q \in \text{Children}(p) \) for which no node in \( T(q) \) is linked by a non-tree edge to an ancestor of \( p \) in \( T(r) \).

\[ ^{c} \text{This definition holds for undirected graphs only.} \]

\[ ^{d} \text{Uppermost Non-tree Neighbor of each Subtree.} \]

\[ ^{e} \text{The notations related to } T(r) \text{ introduced in Subsection 2.4 apply in this case as well.} \]
These theorems can be deduced from Definition 7. Figure 1 depicts a DFS spanning tree $T(r)$ of a connected undirected graph $G$. The root $r$ has exactly two children in $T(r)$. We can remark that the removal of $r$ would disconnect the graph into two connected components: the subgraph induced by 2 and its descendants and the subgraph induced by 1 and its descendants, therefore $r$ is a cut-node. In the same way, the removal of the node 3 would disconnect the subgraph induced by 7 and its descendants from the rest of the graph because no node of $T(7)$ is linked by a non-tree edge to an ancestor of 3.

The following remark shows that cut-nodes and bridges have close relations.

**Remark 1** If an edge $(p,q) \in E$ is a bridge of $G$ then each of its both incident nodes is either a cut-node or a pendent-node (i.e., a node of degree one).

From Theorem 2 and Remark 1, we know that the notion of non-tree neighbor is fundamental to determine cut-nodes and then bridges. So, in order to implement these ideas, we now introduce a new value $u(p)$:

$$u(p) = \min_{x \in T(p)} \left( \{ h(y) : (x,y) \in E - E' \} \cup \{ h(x) \} \right)$$

where $E'$ is the edge set of $T(r)$.

Informally, $u(p)$ corresponds to the minimal value among the height of each node of $T(p)$ and the height of the non-tree neighbors of $T(p)$. For example, in Figure 2, $T(4)$ has two non-tree neighbors: 4 and 6. Indeed, Node 4 is a non-tree neighbor of $T(4)$ because $(4,6)$ is a non-tree edge of $T(r)$ and Node 6 $\notin T(4)$. Conversely, Node 6 is a non-tree neighbor of $T(4)$ because $(4,6)$ is a non-tree edge of $T(r)$ and Node 4 $\in T(4)$. Moreover, 4, 5, and 6 are the nodes of $T(4)$. Thus, $u(4) = \min(\{ h(4), h(6) \} \cup \{ h(4), h(5), h(6) \}) = \min(\{3, 5, 3, 4, 6\}) = 3$. 

Fig. 1 DFS spanning tree in a connected undirected graph.
We now show the formal relation between the Tarjan’s approach and \( u(p) \). Roughly speaking, if \( \forall p \in V \), we know \( h(p), u(p) \), \( Parent(p) \) (for \( p \neq r \)), and \( Children(p) \) then we can easily detect all the cut-nodes and the bridges of \( G \).

**Proposition 1** A node \( p \in V \) is a cut-node if and only if \( p \) satisfies one of the following two conditions:

1. \( (p = r) \land (|Children(p)| \geq 2) \);
2. \( (p \neq r) \land (\exists q \in Children(p) : u(q) \geq h(p)) \).

**Proof.** First, from Theorem 1, we can trivially deduce that 1. is equivalent to the proposition “\( r \) is a cut-node”. Then, for 2., by definition, \( u(p) \) is equal to the lowest height among the height of each node of \( T(p) \) and the height of the non-tree neighbors of \( p \). Now, assume that a node \( p \in V \setminus \{r\} \) is a cut-node. From Theorem 2, we know that there exists a child \( q \) of \( p \) in \( T(r) \) such that no node in \( T(q) \) are linked to an ancestor of \( p \) by a non-tree edge, i.e., each non-tree neighbor of \( T(q) \) has a height in \( T(r) \) greater or equal to the height of \( p \) (From Property 1, each non-tree neighbor of \( T(q) \) is a descendant of \( p \)). Thus, \( u(q) \geq h(p) \). Hence, if \( p \) is a cut-node then 2. is true. With the same arguments we can trivially deduce the reciprocal. \( \Box \)

**Proposition 2** \( \forall p \in V \setminus \{r\} \), Edge \( (p, Parent(p)) \) is a bridge if and only if \( u(p) = h(p) \).

**Proof.** First, we can assert that a bridge \((p, q) \in E' \) (the set of edge of \( T(r) \)) because, by definition, the bridge \((p, q) \) is the only way to go from \( p \) to \( q \) in \( G \) (respectively from \( q \) to \( p \)).

Now, assume that the edge \((p, Parent(p)) \) is a bridge and \( u(p) \neq h(p) \). Then, \( u(p) \) is strictly lower than \( h(p) \) because \( u(p) = \min_{e \in T(p)} \{ h(y) \mid (x, y) \in E - E' \} \cup \{h(x)\} \).
Now, if \( u(p) \) is strictly lower than \( h(p) \) that means that there exists a non-tree edge between an ancestor of \( p \) and a node of \( T(p) \) (by defition of \( u(p) \) and Property 1). Thus, the removal of \((p, \text{Parent}(p))\) does not disconnect \( G \). Contradiction.

Finally, assume that the edge \((p, \text{Parent}(p))\) is not a bridge and \( u(p) = h(p) \). In this case, there exists, at least, one cycle in \( G \) including the edge \((p, \text{Parent}(p))\). As \( T(r) \) is a \( DFS \) spanning tree of \( G \), this cycle is composed with tree edges and one non-tree edge. This non-tree edge links a node of \( T(p) \) to an ancestor of \( p \). Thus, \( u(p) < h(p) \) (by defition of \( u(p) \)). Contradiction. \(\Box\)

3.1.2. Example

We now illustrated Propositions 1 and 2 with these following examples.

**Cut-nodes.** In Figure 2, \( r \) has only one child so \( r \) is not a cut-node. Node 4 satisfies \( u(4) \geq h(2) \) so its parent (Node 2) is a cut-node, in the same way, Node 4 is a cut-node. On the other hand, no child \( p \) of Node 1 satisfies \( u(p) \geq h(1) \) so Node 1 is not a cut-node.

**Bridges.** In Figure 2, Node 4 satisfies \( u(4) = h(4) \) so Edge (4,2) is a bridge. On the other hand, node 5 satisfies \( u(5) = 3 \) and \( h(5) = 4 \) so Edge (5,4) is not a bridge.

3.2. Algorithm \( DFS \)

Obviously, to apply these concepts, we first need to computes a \( DFS \) spanning tree of the network. Several silent self-stabilizing \( DFS \) algorithms have been proposed in the literature, e.g., [14,15,16,17]. In particular, [17] have been proved assuming an unfair daemon. These Algorithms (e.g, [14,15,16,17]) have been written in different computation model but they can be easily adapted to our model because our computation model is simpler (As example, the adaptation of [15] is provided in the appendix). Moreover, using the scheme of proofs of [17], it is easy to prove that any adaptation of these algorithms in our computation model runs assuming an unfair daemon.

All these papers (e.g., [14,15,16,17]) are based on the same idea: each processor computes and stores a representation (using link index or ids) of the path of the \( DFS \) spanning tree leading to the root. Once the algorithm is stabilized, using this path, it is easy for all processor \( p \) to locally deduce several useful parameters\(^1\): \( \text{Parent}(p) \) (only if \( p \neq r \)), \( \text{Children}(p) \), and \( h(p) \) without using any additional variable (i.e., without increasing the memory requirement of the \( DFS \) algorithm).

Thus, in the rest of the paper, we assume the existence of a silent \( DFS \) algorithm running under an unfair daemon and called Algorithm \( DFS \). Moreover, we assume that Algorithm \( DFS \) provides three macros \( \text{Par}_p \) (only for \( p \neq r \)), \( C_p \), and \( \text{Height}_p \). These macros computes \( \text{Parent}(p), \text{Children}(p), \) and \( h(p) \) respectively. Obviously, these macros guarantee the expected result, i.e., \( \text{Par}_p = \text{Parent}(p), C_p = \text{Children}(p), \) and \( \text{Height}_p = h(p) \) only when Algorithm \( DFS \) is stabilized. During the stabilization, we only assume that \( \text{Par}_p \in \text{Neig}_p \cup \{\bot\}, C_p \subseteq \text{Neig}_p, \) and \( \text{Height}_p \in \mathbb{N} \). \( \text{Par}_p = \bot \) means that \( p \) cannot currently distinguish any neighbor as its parent in the \( DFS \) spanning tree.

\(^1\)See Algorithms 3 and 4 of the appendix
3.3. Algorithm \( \mathcal{UNNS} \)

Roughly speaking, Algorithm \( \mathcal{UNNS} \) is a self-stabilizing distributed protocol for computing two predicates: Predicates \( \text{CutNote}_p \) (\( \forall p \in V \)) and \( \text{ParentLink}_{is\_a\_Bridges}_p \) (\( \forall p \in V \setminus \{r\} \)). For the root, using Theorem 1, it is easy to deduce if it is a cut-node since Algorithm \( \text{DFS} \) is stabilized. For the other processes (\( p \neq r \)), Algorithm \( \mathcal{UNNS} \) only consists to computes \( u(p) \) in \( \text{Back}_p \). With \( u(p) \) and using Propositions 1 and 2, we can easily deduce (1) all the non-root cut-nodes of \( G \) and (2) all bridges of \( G \). To that goal, in our algorithm, we use Macros \( \text{Par}_p \) (for \( p \neq r \)), \( C_p \), and \( \text{Height}_p \) as inputs from Algorithm \( \text{DFS} \) to know the parent of each node in \( T(r) \), as well as their children, and \( h(p) \) respectively. Macro \( \text{NonTree\_height}_p \) collects the currently estimated height of each non-tree neighbor of \( p \). Macro \( \text{Children\_back}_p \) collects the currently estimated value of \( u(q) \) for each children \( q \) of \( p \) in \( T(r) \). So, if Macros \( \text{NonTree\_height}_p \), \( \text{Children\_back}_p \), and \( \text{Height}_p \) contain the expected values, then, by definition, \( u(p) = \min(\text{Children\_back}_p \cup \text{NonTree\_height}_p \cup \{\text{height}_p\}) \). Hence, Algorithm \( \mathcal{UNNS} \) has only one action called \( \text{Compute\_back}_p \): if \( \text{Back}_p \neq \min(\text{Children\_back}_p \cup \text{NonTree\_height}_p \cup \{\text{height}_p\}) \), then \( p \) executes \( \text{Back}_p \leftarrow \min(\text{Children\_back}_p \cup \text{NonTree\_height}_p \cup \{\text{height}_p\}) \). Finally, once the system is stabilized, according to Propositions 1 and 2, the predicates \( \text{CutNote}_p \) (\( \forall p \in V \)) and \( \text{ParentLink}_{is\_a\_Bridges}_p \) (\( \forall p \in V \setminus \{r\} \)) allow to determine cut-nodes and bridges of the network, respectively.

As a consequence, in Section 4, we will simply prove that, for all possible executions, Algorithm \( \mathcal{UNNS} \) reaches a terminal configuration and satisfies the following predicate in this configuration:

\[
\text{PRE}_{\mathcal{UNNS}} \equiv (\forall p \in V \setminus \{r\}. \text{Back}_p = u(p)).
\]

\textbf{Algorithm 1} Algorithm \( \mathcal{UNNS} \) for \( p = r \)

\begin{itemize}
  \item \textbf{Input:} \( C_p, \text{Height}_p \): macros from \( \text{DFS} \);
  \item \textbf{Predicate:} \( \text{CutNode}_p \equiv (|C_p| \geq 2) \)
\end{itemize}

\textbf{Algorithm 2} Algorithm \( \mathcal{UNNS} \) for \( p \neq r \)

\begin{itemize}
  \item \textbf{Input:} \( \text{Par}_p \), \( C_p \), \( \text{Height}_p \): macros from \( \text{DFS} \);
  \item \textbf{Variable:} \( \text{Back}_p \in \mathbb{N} \);
  \item \textbf{Macro:} \( \text{Children\_back}_p = \{x \in \mathbb{N} :: (\exists q \in C_p :: \text{Back}_q = x)\} \);
  \item \textbf{Predicate:} \( \text{NonTree\_height}_p = \{x \in \mathbb{N} :: (\exists q \in \text{Net}_p :: q \notin C_p \land \text{Par}_p \neq q \land \text{Height}_q = x)\} \)
  \item \textbf{Predicates:} \( \text{Update\_back}(p) \equiv (\text{Back}_p \neq \min(\text{Children\_back}_p \cup \text{NonTree\_height}_p \cup \{\text{Height}_p\})) \)
  \item \textbf{Predicate:} \( \text{CutNode}_p \equiv (\exists q \in C_p :: \text{Back}_q \geq \text{Height}_p) \)
  \item \textbf{Predicate:} \( \text{ParentLink}_{is\_a\_Bridges}_p \equiv (\text{Back}_p = \text{Height}_p) \)
  \item \textbf{Actions:} \( \text{Compute\_back}(p) :: \text{Update\_back}(p) \rightarrow \text{Back}_p := \min(\text{Children\_back}_p \cup \text{NonTree\_height}_p \cup \{\text{Height}_p\}) \)\)
\end{itemize}
3.4. Protocol Composition

Algorithm $\mathcal{UNN}\mathcal{S}$ and Algorithm $\mathcal{DFS}$ run concurrently. For Algorithm $\mathcal{UNN}\mathcal{S}$, Algorithm $\mathcal{DFS}$ is shown as a black box providing several outputs: Macros $\text{Par}_p$, $C_p$, and $\text{Height}_p$. Algorithm $\mathcal{UNN}\mathcal{S}$ access to these macros in read only. So, Algorithm $\mathcal{UNN}\mathcal{S}$ and Algorithm $\mathcal{DFS}$ are composed following collateral composition which defined in [13] as follows:

Definition 10 (Collateral Composition) Let $S_1$ and $S_2$ be programs such that no variables written by $S_2$ appears in $S_1$. The collateral composition of $S_1$ and $S_2$, denoted $S_1 \odot S_2$, is the program that has all the variables and all the actions of $S_1$ and $S_2$.

From now on, we note Algorithm $\mathcal{UNN}\mathcal{S} \odot \mathcal{DFS}$ the collateral composition of Algorithm $\mathcal{UNN}\mathcal{S}$ and Algorithm $\mathcal{DFS}$. The next definitions and theorem give the general scheme for the proof of self-stabilization of Algorithm $\mathcal{UNN}\mathcal{S} \odot \mathcal{DFS}$.

Let $L_1$ and $L_2$ be predicate over the variables of $S_1$ and $S_2$, respectively. In the composite algorithm, $L_1$ will be established by $S_1$, and subsequently, $L_2$ will be established by $S_2$. We now define a fair composition with respect to both programs, and define what it means for a composite algorithm to be self-stabilizing.

Definition 11 (Fair Execution) An execution $e$ of $S_1 \odot S_2$ is fair with respect to $S_i$ ($i \in \{1, 2\}$) if one of these conditions holds:

1. $e$ is finite;
2. $e$ contains infinitely steps of $S_i$, or contains an infinite suffix in which no action of $S_i$ is enabled.

Definition 12 (Fair Composition) The composition $S_2 \odot S_1$ is fair with respect to $S_i$ ($i \in \{1, 2\}$) if every execution of $S_2 \odot S_1$ is fair with respect to $S_i$.

Theorem 3 $S_2 \odot S_1$ stabilizes to $L_2$ if the following four conditions hold:

1. Program $S_1$ stabilizes to $L_1$;
2. Program $S_2$ stabilizes to $L_2$ if $L_1$ holds;
3. Program $S_1$ does not change variables read by $S_2$ once $L_1$ holds;
4. The composition is fair with respect to both $S_1$ and $S_2$.

4. Proof of Correctness

In this section, we prove that the composite algorithm $\mathcal{UNN}\mathcal{S} \odot \mathcal{DFS}$ is self-stabilizing for the predicate $\text{PRE}_{\mathcal{UNN}\mathcal{S}}$. Thus, we first recall that the daemon is assumed to be unfair. Moreover, we assume that Algorithm $\mathcal{DFS}$ is silent and self-stabilizing, tolerates an unfair daemon, and outputs three macros: $\text{Par}_p$ (for $p \neq r$ only), $\text{Height}_p$, and $C_p$.

Lemma 1 Every execution of Algorithm $\mathcal{UNN}\mathcal{S} \odot \mathcal{DFS}$ has a finite number of moves.
Proof. We have assumed that Algorithm $D\mathcal{F}S$ is silent and runs assuming an unfair daemon. So, every execution of Algorithm $D\mathcal{F}S$ has a finite number of moves. Hence, we have just to prove that, between two configurations of Algorithm $D\mathcal{F}S$, Algorithm $U\mathcal{N}\mathcal{S}$ can execute a finite number of steps only.

By hypothesis, we consider that Algorithm $D\mathcal{F}S$ does not make any action. Then, we can assume that $\forall p \in V$ the values returned by the macros $\text{Par}_p$ (for $p \neq r$), $C_p$, $\text{Height}_p$ are set. Let $G_c = (V_c, E_c)$ be the partial directed graph of $G$ where $E_c = \{(p, q) \in E : p \neq r \land \text{Par}_p = q\}$ for an arbitrary configuration of Algorithm $D\mathcal{F}S$. Thus, $G_c$ is the partial directed graph of $G$ computed by Algorithm $D\mathcal{F}S$. We focus on $G_c$ because, from Predicate $\text{Update\_back}$, we know that the updating of $\text{Back}_p$ for a processor $p$ can only cause the execution of $\text{Compute\_back}$ of the processor pointed out by the macro $\text{Par}_p$, if it exists (remember that $\text{Par}_p$ may be equal to $\bot$).

In any “system” $G_c$, we are interested in the causes of the moves. Thus, a move can be caused by:

- An initial configuration.
- A change in the state of a neighbor.

We call an initial move a move caused by an initial configuration. By definition, the number of initial moves is bounded (by $n - 1$, since $r$ has no action in Algorithm $U\mathcal{N}\mathcal{S}$). So, we have to show that the number of the other moves is also bounded.

For each connected component $CC_i = (V_i, E_i)$ of $G_c$, two cases are possible:

1. $CC_i$ is a tree. Then, in the worst case, each processor $p \in V_i \setminus \{r\}$ can execute $\text{Compute\_back}(p)$ as an initial move (the program of $r$ contains no action in Algorithm $U\mathcal{N}\mathcal{S}$). Moreover, the updating of $\text{Back}_p$ can only cause the execution of $\text{Compute\_back}$ of the processor pointed out by the macro $\text{Par}_p$, if it exists (see Predicate $\text{Update\_back}$). Now, we can remark that, for the nodes of $V_i$, the cause relationship of the action $\text{Compute\_back}$ is acyclic (indeed, the Backs’ updatings go up in $T(r)$ following the $\text{Par}$ variables) and the source of all chains of $\text{Back}_p$ updating are always initial moves. Thus, the length each chain of $\text{Back}_p$ updating in $CC_i$ is bounded by the height of $CC_i$. Thus, if $CC_i$ is a tree, the number of $\text{Compute\_back}$ moves in $CC_i$ is finite.

2. $CC_i$ is not a tree. First, as $\forall p \in V_i$, $\text{Par}_p$ pointed out, at most, one processor and $CC_i$ is connected, we can remark that:

   (a) $CC_i$ contains one cycle only. We note $C$ this cycle and $c$ its number of processors.

   (b) $\forall p \in V_i$, $p$ is not a sink, i.e., a processor with no outgoing edge. In particular, that means that $p \neq r \land \text{Par}_p \neq \bot$.

In the worst case, each processor $p \in V_i$ can execute $\text{Compute\_back}(p)$ as an initial move.

Consider a enabled processor $q \in V_i$ which does not belong to $C$. The execution of $\text{Compute\_back}(q)$ can only cause the execution of the action $\text{Compute\_back}$ of the processor pointed out by the macro $\text{Par}_q$ (see Predicate $\text{Update\_back}$) and so on. In the worst case, these executions follow a path from $q$ to a processor of $C$. 

Now, consider the case where the action $\text{Compute}$ of a processor $q$ in $C$ is or becomes enabled. The execution of $\text{Compute}(q)$ can only activate the processor pointed out by $\text{Par}_q$ (see Predicate $\text{Update}$) and follows $C$. However, these executions propagate the same value in the $\text{Back}$ variables. Thus, in the worst case, the processor $q$ can, at most, induce $c - 1$ executions of $\text{Compute}$ actions (one for each other node of $C$). Indeed, a processor becomes enabled in order to assign a new value to its $\text{Back}$ variable only. Thus, even if $CC_i$ is not a tree, the number of $\text{Compute}$ moves in $CC_i$ is also finite.

\[\square\]

Let $p \in V$. Let $f$ be the leaf with the upper height in $T(p)$. We define $d(p)$ as follows:

$$d(p) = h(f) - h(p).$$

Thus, $d(p)$ represents the distance between $p$ and the upper height leaf of its induced subtree.

**Lemma 2** Algorithm $UNNS$ stabilizes to $PRE_{UNNS}$ if $PRE_{DFS}$ holds.

**Proof.** We begin the proof with some claims. First, by Lemma 1, we know that $UNNS \circ DFS$ reaches a terminal configuration in a finite number of moves and, in this configuration, $PRE_{DFS}$ holds. Assuming $PRE_{DFS}$ holds, all the edges $(p, Par_p)$ such that $p \in V \setminus \{r\}$ shape a $DFS$ spanning tree $T(r)$ of $G$, the macro $C_p$ equals $\text{Children}(p)$, and the macro $\text{Height}_p$ returns $h(p)$. Since the system reaches a configuration where no action is enabled, the predicate $\text{Update}(p)$ is false for each $p \in V \setminus \{r\}$.

Thus, we have to prove that, in this configuration, $\forall p \in V \setminus \{r\}, \text{Back}_p = u(p)$. We prove that by induction on $d(x)$ in $T(r)$.

Let $L(r)$ the set of leaves of $T(r)$. In the terminal configuration, $\forall f \in L(r) (d(f)=0)$, because $\text{Children}_f = \emptyset$, $\text{Back}_f = \min (\text{NonTree}_f \cup \{\text{Height}_f\})$. Now, $\text{NonTree}_f = \bigcup_{v \in Ngy \setminus \{p \mid r\}} \{h(v)\}$, $\text{Children}_f = \emptyset$, and $T(f) = \{f\}$. Then, $\text{Back}_f = \min_{e \in T(f)} \{h(y) : (x, y) \in E - E' \cup \{h(x)\}\} = \{h(y) : (x, y) \in E - E' \cup \{h(x)\}\}$.

Now, assume that for each node $p \in V \setminus \{r\}$, such that $d(p) \leq k (k \geq 0)$, we have $\text{Back}_p = u(p)$. Consider the nodes $q \in V \setminus \{r\}$, such that $d(q) = k + 1$. In the terminal configuration, $\text{Back}_q = \min \left( \text{Children}_q \cup \text{NonTree}_q \cup \{\text{Height}_q\} \right)$. By induction assumption, $\text{Children}_q = \bigcup_{v \in \text{Children}_q} \{u(v)\}$, $\text{Height}_q = \bigcup_{v \in \text{Children}_q} \{h(v)\}$, $\text{NonTree}_q = \bigcup_{v \in \text{Children}_q \setminus \{q\}} \{h(v)\}$, and $T(q) = \{h(x) : (x, y) \in E - E' \cup \{h(x)\}\}$. Thus, $\text{Back}_q = \min_{e \in T(q)} \{h(y) : (x, y) \in E - E' \cup \{h(x)\}\}$.

Now, $T(q) = \{h(x) : (x, y) \in E - E' \cup \{h(x)\}\}$, and follows $C_q$ only if $q \in V \setminus \{r\}$, such that $d(q) = k + 1$. Hence, $\text{Back}_q = u(q)$. This property is true for each processor $p$ such that $d(p) \leq k + 1$. With $k = H - 1$ (because $\text{Compute}$ does not exist in the program of $r$) the lemma holds.

\[\square\]

**Theorem 4** Algorithm $UNNS \circ DFS$ stabilizes to $PRE_{UNNS}$ under the unfair daemon.

**Proof.** From the following four observations and Theorem 3, the result holds.
1. By assumption, Algorithm $\mathcal{DFS}$ stabilizes to $\text{PRE}_{\mathcal{DFS}}$.

2. By Lemma 2, Algorithm $\mathcal{UNNS}$ stabilizes to $\text{PRE}_{\mathcal{UNNS}}$ if $\text{PRE}_{\mathcal{DFS}}$ holds.

3. Because Algorithm $\mathcal{DFS}$ is silent, trivially, we can claim that Algorithm $\mathcal{DFS}$ does not change variables read by $\text{PRE}_{\mathcal{UNNS}}$ once $\text{PRE}_{\mathcal{DFS}}$ holds.

4. From Lemma 1, we know that every execution of $\mathcal{UNNS} \circ \mathcal{DFS}$ is finite. Thus, the composition $\mathcal{UNNS} \circ \mathcal{DFS}$ is fair with respect to both $\text{PRE}_{\mathcal{DFS}}$ and $\text{PRE}_{\mathcal{UNNS}}$ (see Definition 11).

Finally, from Theorem 4, Propositions 1 and 2, we can claim the following theorem.

Theorem 5 Algorithm $\mathcal{UNNS} \circ \mathcal{DFS}$ is self-stabilizing and detects all cut-nodes and bridges of $G$.

Corollary 1 After Algorithm $\mathcal{UNNS} \circ \mathcal{DFS}$ terminates, a node $p \in V$ is a cut-node if and only if $p$ satisfies $\text{CutNode}_p$.

Corollary 2 After Algorithm $\mathcal{UNNS} \circ \mathcal{DFS}$ terminates, $\forall p \in V \setminus \{r\}$, $(p, \text{Par}_p)$ is a bridge if and only if $p$ satisfies $\text{ParEndLeaf}_{k+1}$ and $\text{Bridge}_p$.

5. Complexity

In order to compare our algorithm with solutions proposed in the literature, we compute the time complexity of Algorithm $\mathcal{UNNS}$ after Algorithm $\mathcal{DFS}$ terminates. Thus, in this section, we assume the presence of a $\mathcal{DFS}$ spanning tree of $G$, $T(r)$. We also presented the space complexity of our solution.

5.1. Time Complexity

Theorem 6 Algorithm $\mathcal{UNNS}$ needs $O(H)$ rounds to reach a terminal configuration after Algorithm $\mathcal{DFS}$ terminates.

Proof. Since we assume that Algorithm $\mathcal{DFS}$ is terminated, a $\mathcal{DFS}$ tree of $G$, $T(r)$, has been computed: $C_p$, and $\text{Height}_p$ are constant ($\forall p \in V$) and $\text{Par}_p$ too ($\forall p \in V \setminus \{r\}$). Hence, $\forall p \in V \setminus \{r\}$, if $\text{ComputeBack}(p)$ is disabled then it can become enabled if and only if at least one of its children $q$ in $T(r)$ has modified its variable $\text{Back}_q$ (see Predicate $\text{UpdateBack}(p)$).

We prove this lemma by induction on $d(x)$ (distance from $x$ to its farther leaf) in $T(r)$. Let $f \in L(r)$, the set of leaves of $T(r)$ ($d(f) = 0$), Action $\text{ComputeBack}(f)$ depends on $\text{Height}$ variables only, so after one round, $\text{ComputeBack}(f)$ is disabled forever.

Now, assume that $\forall p \in V \setminus \{r\}$, such that $d(p) \leq k$ ($k \geq 0$), we have $\text{ComputeBack}(p)$ disabled forever and $\text{Back}_p$ is now constant after, at most, $d(p) + 1$ rounds.

For each $q \in V \setminus \{r\}$, $\text{UpdateBack}(q)$ uses only $\text{Back}$ values of its children and $\text{Height}$ variables (which are constant). So, during the round $k+2$, each $q \in V \setminus \{r\}$ such
that $d(q) = k+1$ reads $\text{Back}$ and $\text{Height}$ values which are constant from now on. If $\text{Compute} \backslash \text{Back}(q)$ is disabled, it will remain forever. Otherwise $\text{Compute} \backslash \text{Back}(q)$ is continuously enabled until $q$ executes it. So, after the round $k+2$, $\text{Compute} \backslash \text{Back}(q)$ is disabled forever. At the end of the round $H$ ($\forall q \in V \setminus \{r\}$, $d(q) < H$) no $\text{Compute} \backslash \text{Back}$ action is enabled in the system.

**Theorem 7** Algorithm $UNNS$ needs $O(n^2)$ moves to reach a terminal configuration after Algorithm $DFS$ terminates.

**Proof.** In order to prove this time complexity, we use the notions of causes of the moves again. We recall that a move can be caused by:

- An initial configuration.
- A change in the state of a neighbor.

We call an initial move: a move caused by an initial configuration. As we assume Algorithm $DFS$ is terminated, we can consider that, $\forall p$, the values returned by the macros $\text{Par}_p$ (for $p \neq r$), $\text{C}_p$, and $\text{Height}_p$ are also set. Hence, there exists a $DFS$ spanning tree of $G$ rooted at $r$, $T(r) = (V', E')$ such that $E' = \{(p, \text{Par}_p); p \in V \setminus \{r\}\}$. In the worst case, each processor $p \in V \setminus \{r\}$ can execute $\text{Compute} \backslash \text{Back}(p)$ as an initial move. So, the total number of execution of initial $\text{Compute} \backslash \text{Back}$ is in $O(n)$ moves. Then, the updating of the variable $\text{Back}_p$ of a processor $p$ can only cause the execution of $\text{Compute} \backslash \text{Back}$ of its parent in $T(r)$, i.e., $\text{Par}_p$ (see Predicate $\text{Update} \backslash \text{Back}$). Moreover, we can remark that the cause relationship of the action $\text{Compute} \backslash \text{Back}$ is acyclic (indeed, the $\text{Backs'}$ updatings go up in $T(r)$ following the $\text{Par}$ variables) and the source of all chains of $\text{Backs'}$ updating is always an initial move. Thus, the length of each chain of $\text{Backs'}$ updating is bounded by $H$, the height of $T(r)$ ($H \leq n$). Hence, the number of executions of $\text{Compute} \backslash \text{Back}$ is in $O(n^2)$ after Algorithm $DFS$ terminates. \hfill $\square$

5.2. Space Complexity

Algorithm 1 contains no variable. Algorithm 2 contains on variable only: $\text{Back}$. $\forall p \in V \setminus \{r\}$. $\text{Back}_p$ is used to store the height of $p$ or the height of one of its ancestor. So, the value of $\text{Back}_p$ can be bounded by $n$ and the following theorem is obvious.

**Theorem 8** The memory requirement of Algorithm $UNNS$ is $O(\log(n))$ bits per processor where $n$ is the number of processors.

6. Conclusion

We have presented a silent, distributed, and self-stabilizing algorithm which detects cut-nodes and bridges in arbitrary rooted networks. This algorithm must be composed with a silent self-stabilizing algorithm computing a $DFS$ spanning tree of the network like [16,17]. Once the $DFS$ spanning tree is computed, our algorithm needs only $O(H)$ rounds and $O(n^2)$ moves to reach a terminal configuration. This time complexity is equivalent to the best already proposed solutions. We have shown that our solution works under an unfair
daemon, i.e., the weakest scheduling assumption. Moreover, the memory requirement of our algorithm is $O(\log(n))$ bits per processor (without taking account of variables used for the spanning tree computing). Until now, this is the protocol with the lowest memory requirement solving this problem.

References

Algorithm 3 Algorithm DFS for \( p = r \)
Input: \( \text{Net}_{gp} \): set of neighbors (locally ordered);
Constant: \( Path_p = \bot \);
Macro:
\( Height_p = |Path_p| - 1 \);
\( C_p = \{ q \in \text{Net}_{gp} : (Path_p \oplus \alpha_p(q)) = Path_q \} \);

Algorithm 4 Algorithm DFS for \( p \neq r \)
Input: \( \text{Net}_{gp} \): set of neighbors (locally ordered);
Constant: \( N \geq n \);
Variable: \( Path_p \): list of, at most, \( N \) ordered items \( \in \{ \bot \} \cup \mathbb{N} \);
Macro:
\( \text{Read}_p = \bigcup_{q \in \text{Net}_{gp}} \{ \text{right}_N (Path_q \oplus \alpha_p(q)) \} \);
\( Height_p = |Path_p| - 1 \);
\( Path_p = q \iff \exists ! q \in \text{Net}_{gp} : (Path_q \oplus \alpha_p(q)) = Path_p \), \( \bot \) otherwise,
\( C_p = \{ q \in \text{Net}_{gp} : (Path_p \oplus \alpha_p(q)) = Path_q \} \);
Predicates:
\( \text{Update}_p \equiv (Path_p \neq \min_{<_{\alpha_p}} (\text{Read}_p)) \)
Action:
\( \text{Compute}_p \equiv \quad \text{Update}_p \rightarrow Path_p := \min_{<_{\alpha_p}} (\text{Read}_p) \);

First, in [15], authors assume that \( \forall p \in V, \forall q \in \text{Net}_{gp}, p \) knows \( \alpha_p(q) \), i.e., the index of Edge \( (p, q) \) in \( \text{Net}_{gp} \) (for details see [15]). Then, Algorithm DFS works as follows: the memory of any processor \( p \) consists of a path field denoted by \( Path_p \). The root \( r \) has its constant \( Path_r \) equal to \( \bot \). Any other processor \( p \) computes its path field according to the path fields of its neighbors. From the path \( Path_q \), read by \( p \) from the neighbor \( q \), \( p \) derives a path by concatenating \( Path_q \) with \( \alpha_q(p) \) (noted \( Path_q \oplus \alpha_q(p) \)). Then, \( p \) chooses its path to be the minimal path (according to the lexicographical order \( <_{\alpha_p} \)) among the paths derived from its neighbors’ paths. For any processor \( p \), \( Path_p \) contains a sequence of at most \( N \) items \( (N \geq n) \) where an item is \( \bot \) or an edge index. Indeed, \( Path_p \) may describe the longest elementary path that is possible in \( G \). Thus, the notation \( \text{right}_k(w) \) refers to the sequence of the \( k \) least significant items of \( w \).