# **Modeling Real-time Systems**

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Modeling Real-time Systems

### **Motivation - Modeling**

Modeling plays a central role in systems engineering

- Can profitably replace experimentation on actual systems
- Can provide a basis for rigorous system development and implementation (model-based approaches).

### Modeling real-time systems

- Raises hard problems about concepts, languages and their semantics e.g. What is an architecture? What is a scheduler? How synchronous and asynchronous systems are related?
- Requires a deep understanding of basic system design issues such as development methodologies (combination of techniques and tools, refinement) and architecture design principles

### It's not just playing with graphical tools ....

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### Outline

# **Key Research issues**

- Modeling Real-time systems
- From application SW to implementations
- Component-based construction

## The modeling framework

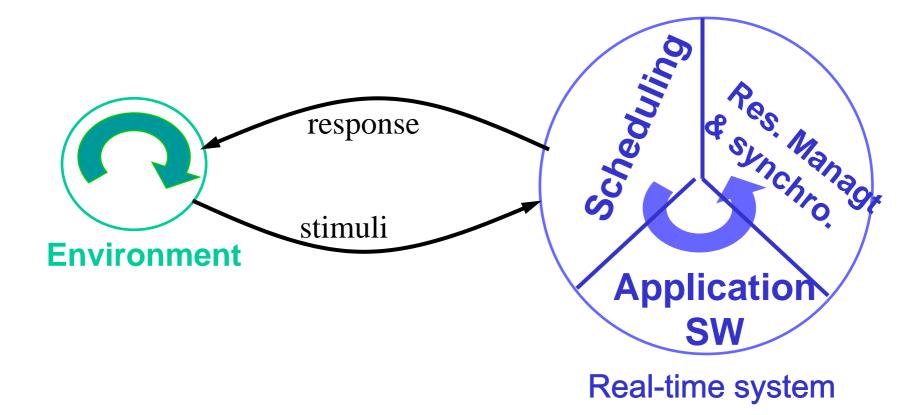
- Principles
- Interaction models
- Scheduler modeling
- Timed models with priorities

# Discussion

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#### Modeling real-time systems



#### Thesis :

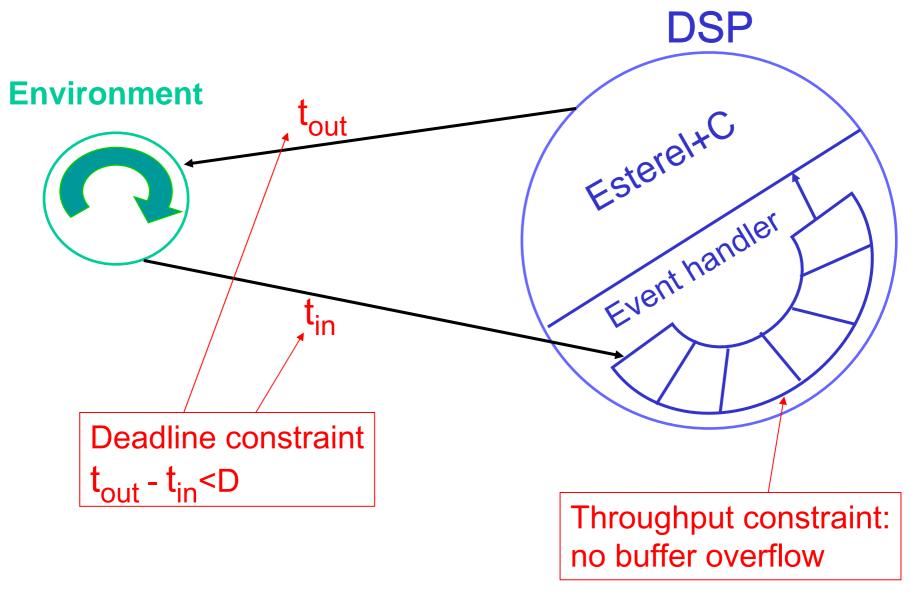
A Timed Model of a RT system can be obtained by "composing" its application SW with timing constraints induced by both its execution and its external environment

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### Modeling real-time systems

	Application SW	Timed model
DESCRIPTION	Reactive machine (untimed)	Reactive machine + External Environment + Execution Platform
TIME	Reference to physical (external) time	Quantitative (internal) time Consistency pbs - timelocks
TRIGGERING	Timeouts to control waiting times $\frac{2e}{TO(5)}$	Timing constraints on interactions ?e [0,6] 0 !e [0,4]
ACTIONS L Sifakis	No assumption about Execution Times Platform-independence Modeling Real-time Systems	Assumptions about Execution Times Platform-dependence BTSS04

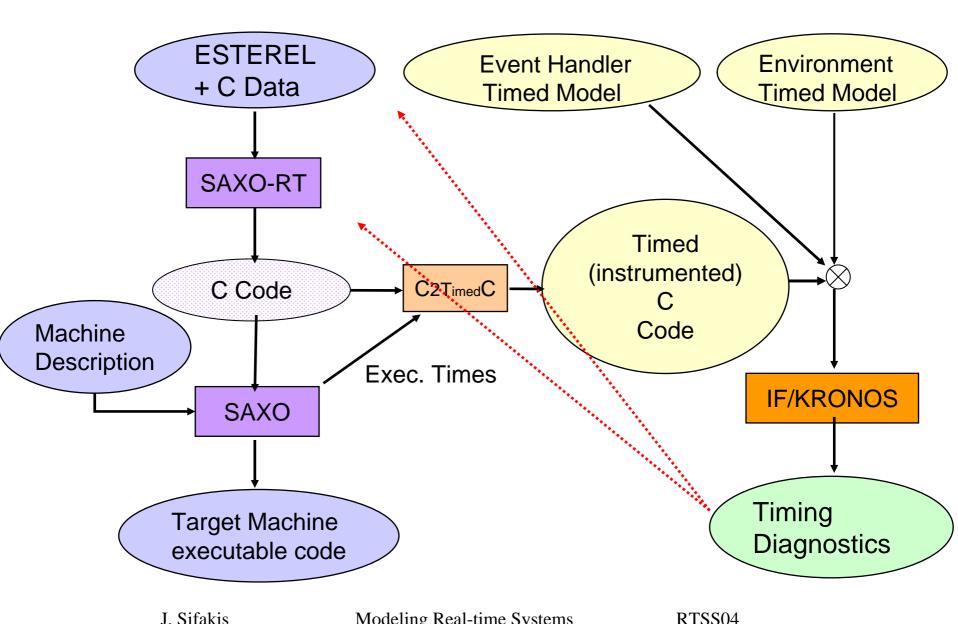
#### Modeling real-time systems – Taxys (1)



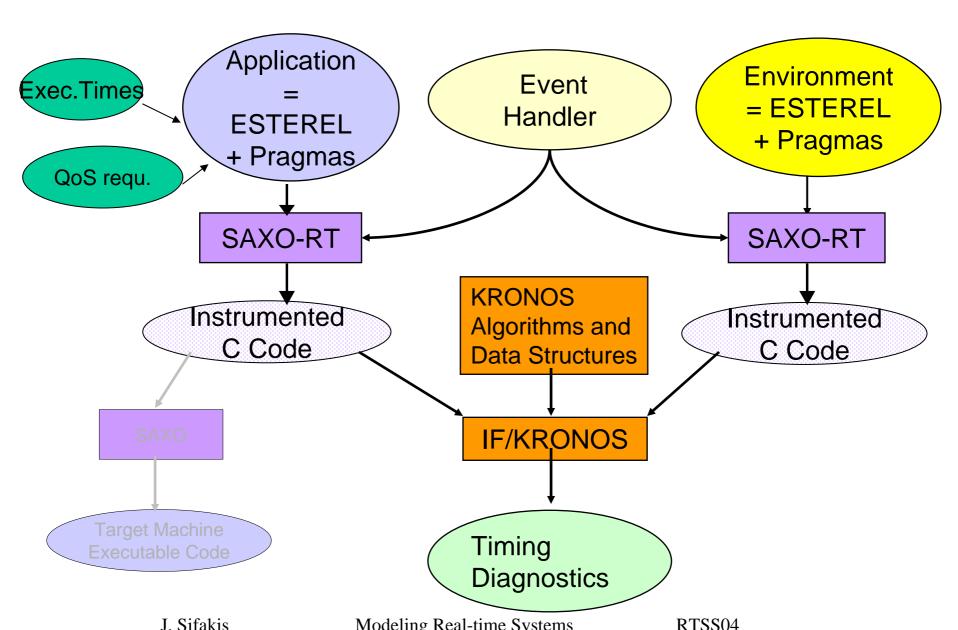
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Modeling real-time systems – Taxys (2)



#### Modeling real-time systems – Taxys(3)



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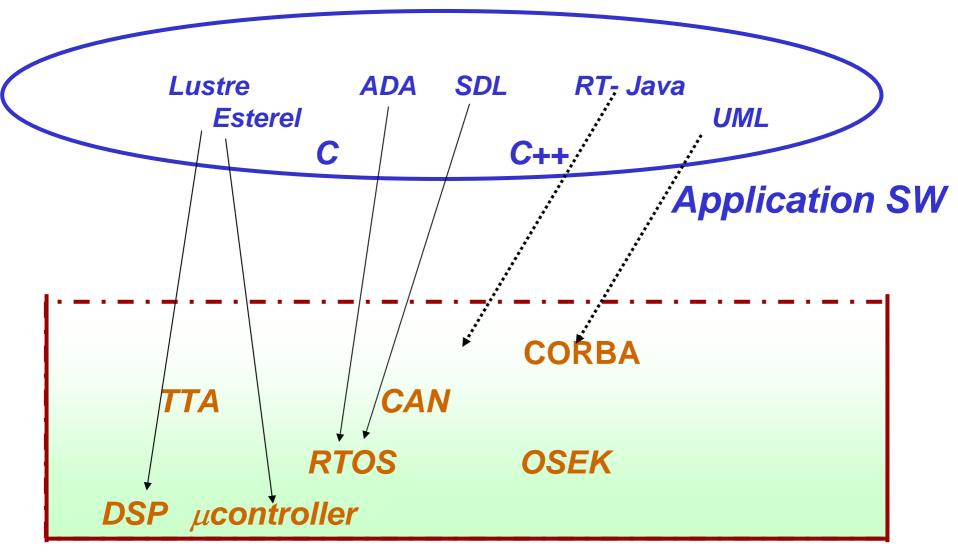
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#### From application SW to implementations

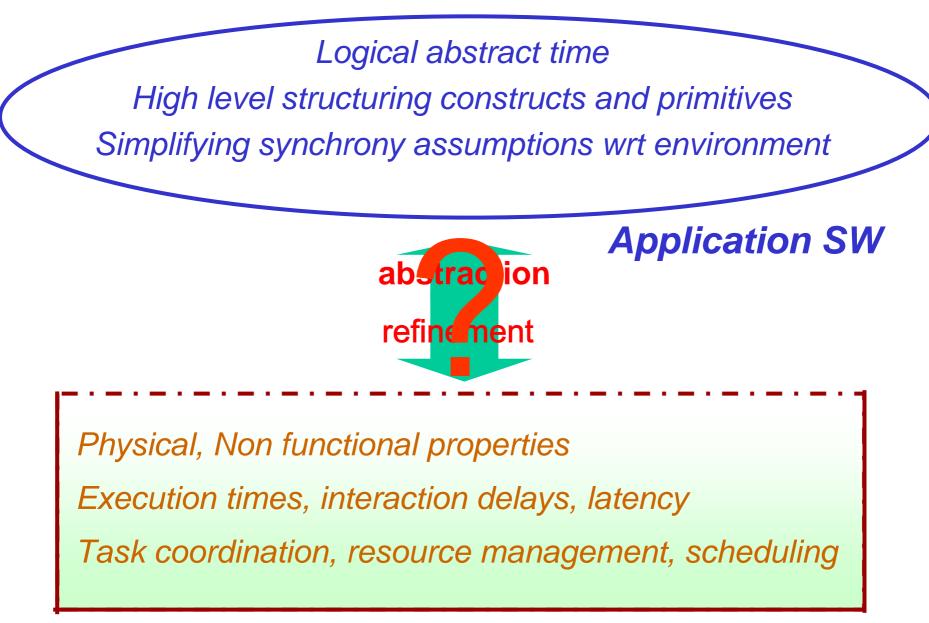


#### Implementation RTSS04

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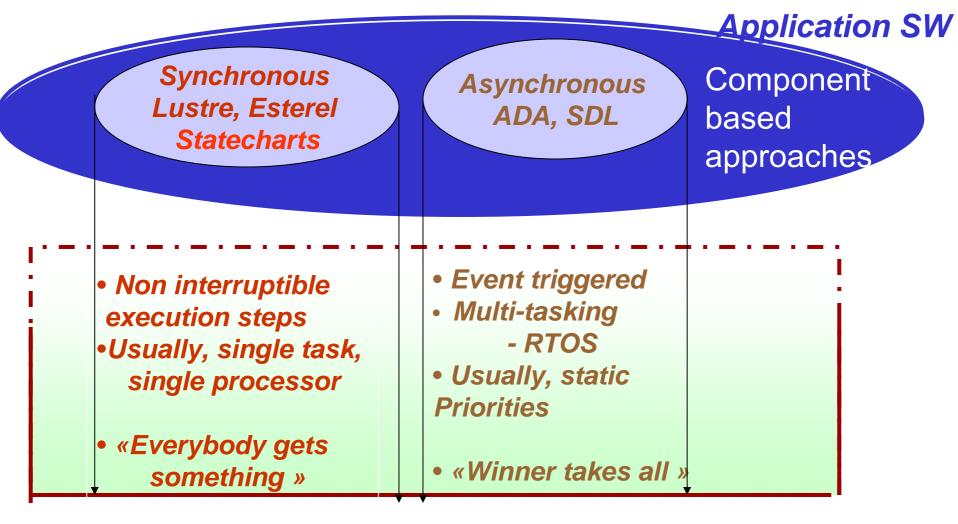
From application SW to implementations



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Implementation

# From application SW to implementations – synchronous vs. asynchronous



### Implementation

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  - **Component-based construction**

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**Component-based construction** 

**Construction problem:** Given a component **C** and a property **P** find **C**' and **glue** such that



### Two key inter-dependent issues:

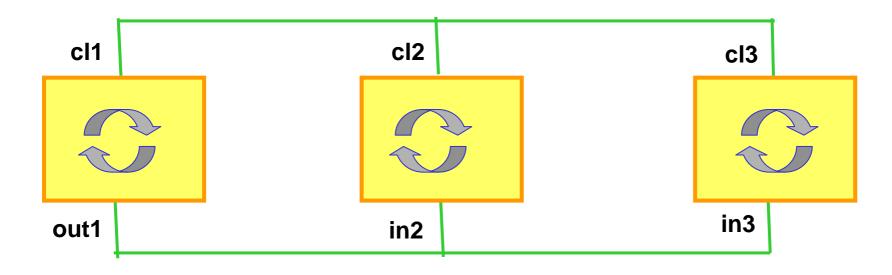
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- Heterogeneity of the glue need for a unified theory
- Methods guaranteeing correctness by construction for at least some basic properties e.g. deadlockfreedom

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#### Component-based construction – the glue

### Assign meaning to diagrams



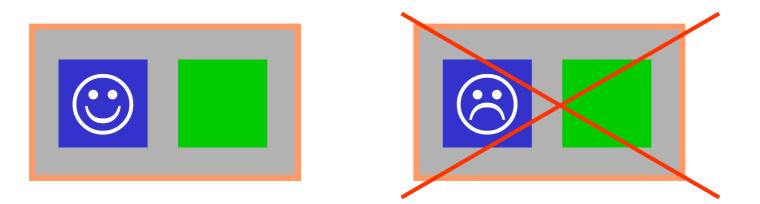
- What are the possible interactions?
- How computation threads of components are related e.g. synchronous vs. asynchronous execution

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Correctness by construction - Composability

Make the new without breaking the old

**Composability rule**: guarantees that a component property is preserved when merged in an environment



- We badly need composability results
- Property stability phenomena are poorly understood
  - feature interaction
  - non composability of scheduling algorithms

Correctness by construction - Compositionality

Build correct systems from correct components

**Compositionality rule**: guarantees that if the components of a system meet a given property then this property is preserved by composition



We need compositionality results that preserve properties other than safety properties e.g. progress properties

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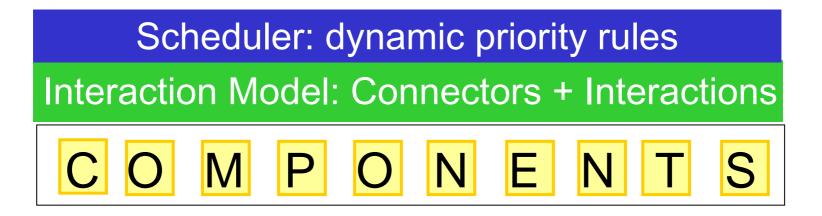
# Discussion

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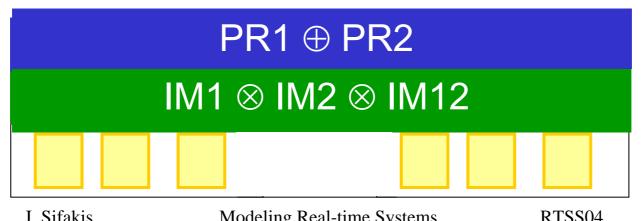
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Modeling framework principles

Layered description – separating the issues



**Composition (incremental description)** 



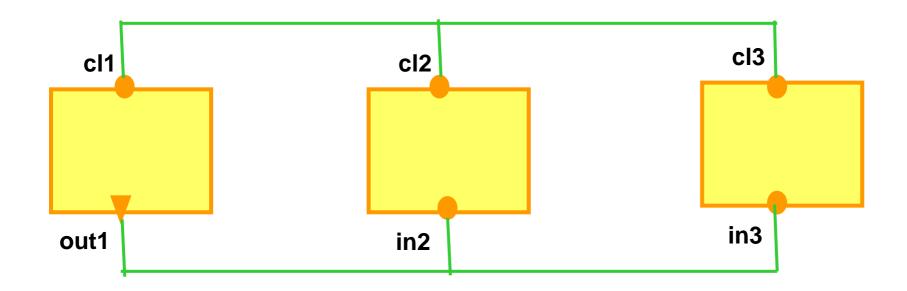
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Modeling framework principles – Heterogeneous interaction

Interactions may

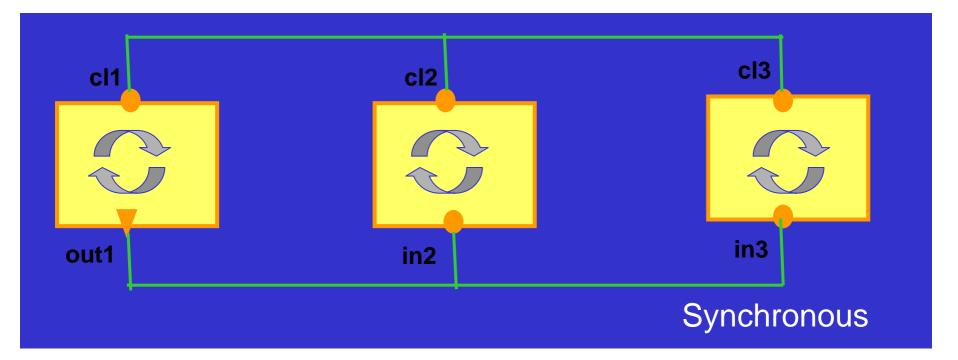
- be atomic or non atomic
- involve strong or weak synchronization



- Strong synchronization: all the participants must agree Example: only {cl1,cl2,cl3} is possible
- Weak synchronization: interaction is initiated by a "leader" Example: {out1,in2,in3}, {out1,in2}, {out1,in3}, {out1}, are possible Usifakis Modeling Real-time Systems RTSS04

Modeling framework principles – Heterogeneous execution

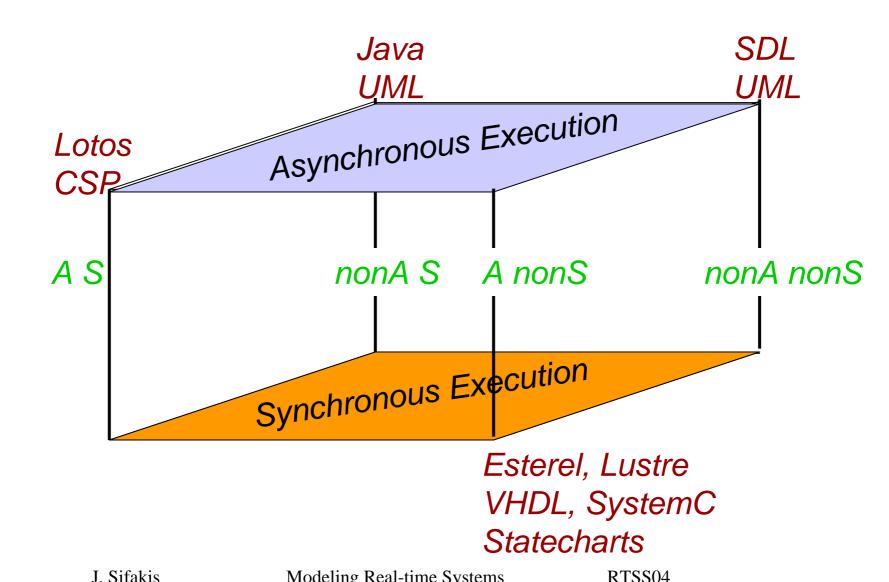
- Asynchronous: independent threads modulo interaction constraints
- **Synchronous:** additional strong synchronization constraints enforced by using scheduling mechanisms



#### Synchronous = Asynchronous + Scheduling Modeling Real-time Systems RTSS04

#### Modeling framework principles - Example

A: Atomic interaction S: Strong synchronization



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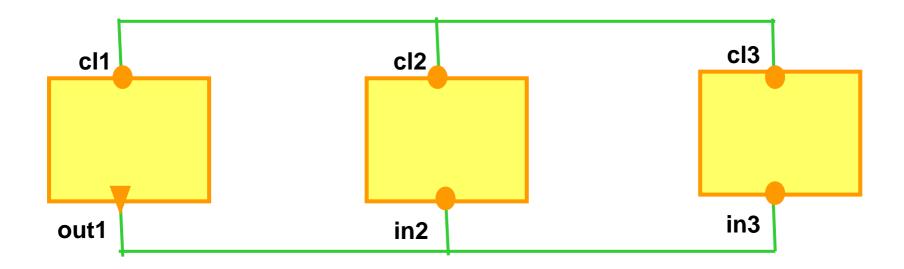
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#### Interaction models

- **Connectors** are maximal sets of compatible actions -Interactions are subsets of connectors
- Actions types (complete , incomplete ) determine the set of possible interactions : interactions are either maximal or contain some complete action

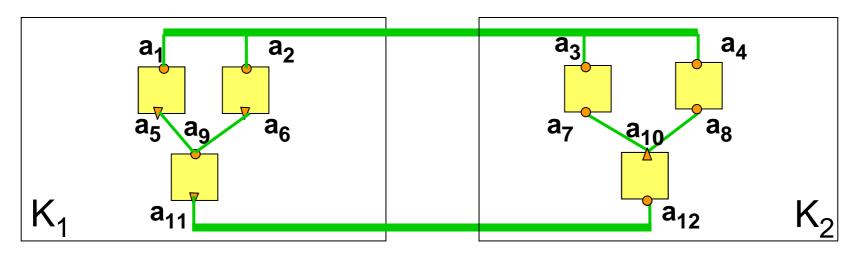


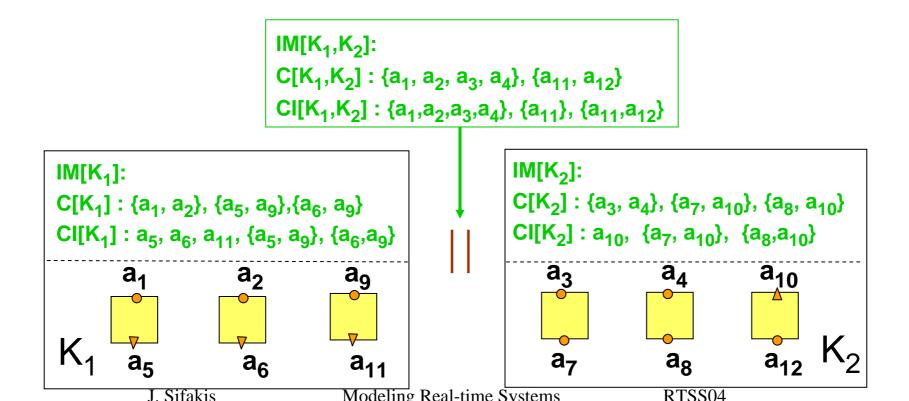
Interactions: {cl1,cl2,cl3}, {out1}, {out1,in2}, {out1,in3}, {out1,in2, in3}

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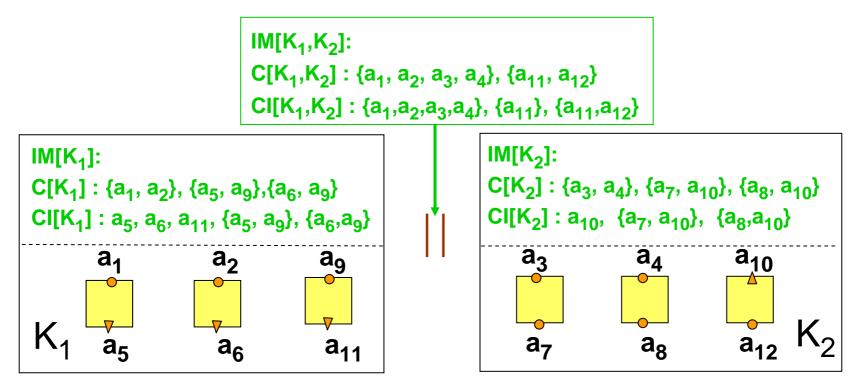
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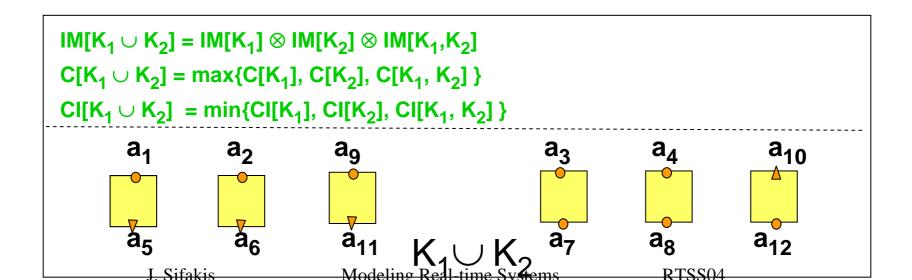
#### **Interaction models - Composition**





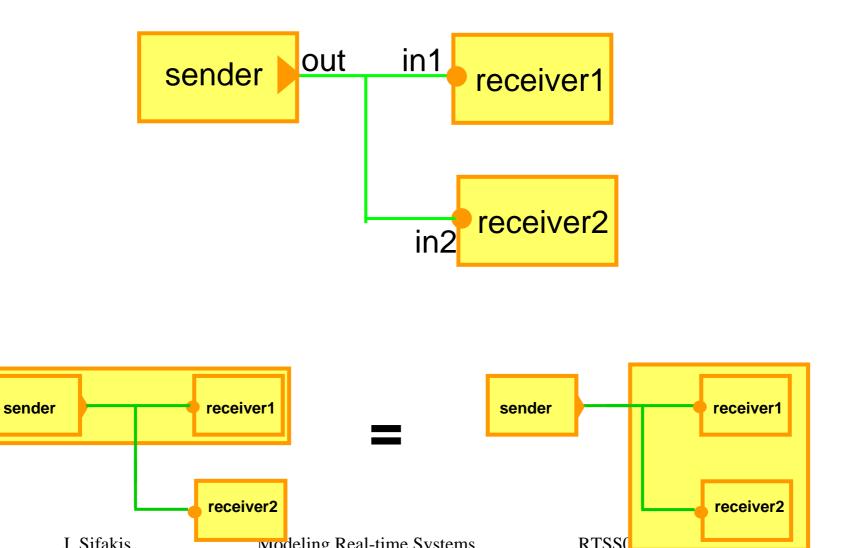
#### Interaction models – Composition (2)





#### Interaction models – Associativity

#### Composition is associative and commutative



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# Discussion

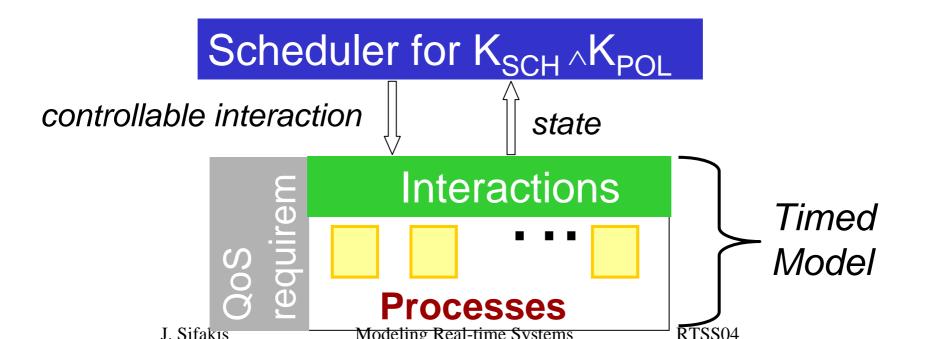
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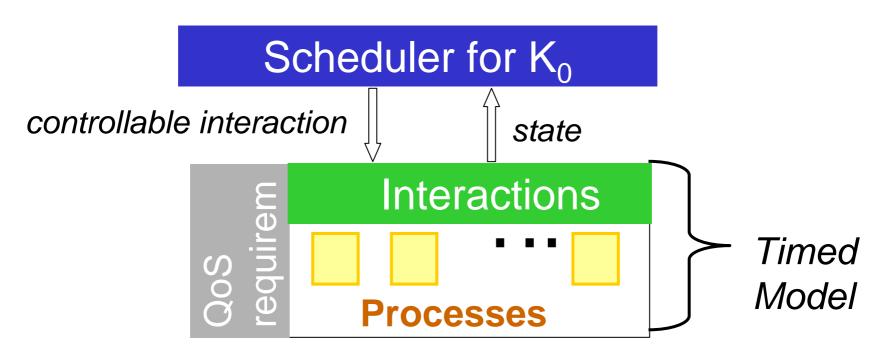
#### Schedulers - Their role

A scheduler is a controller that restricts access to resources by triggering controllable interactions so as to respect timing constraints (state predicates)  $K_0 = K_{SCH} \wedge K_{POL}$ 

- K<sub>SCH</sub> timing constraints on process actions
- K<sub>POL</sub> scheduling policy



Schedulers - Control invariants



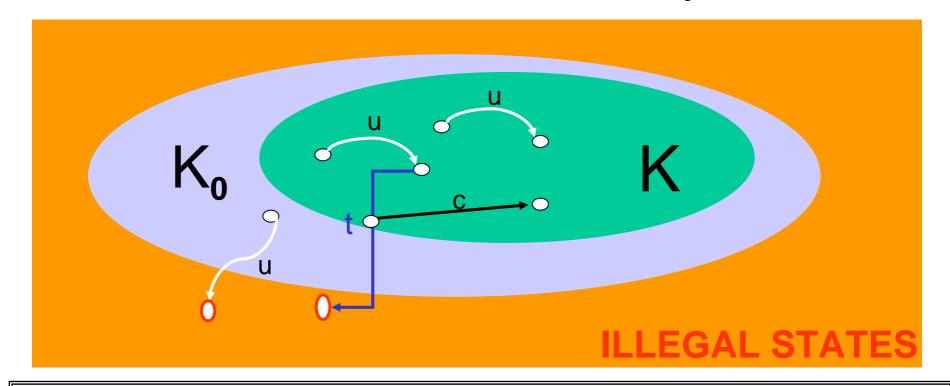
A scheduler for  $K_0$  can be defined by a **control invariant**  $K \Rightarrow K_0$ 

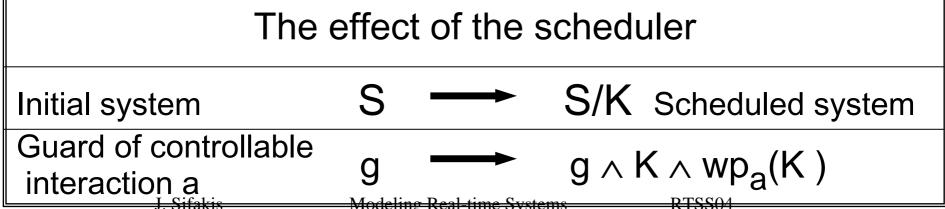
- K contains only deadlock-free states for all processes
- K cannot be violated by uncontrollable interactions
- If from some state time progress can violate K, then there exists from this state controllable action preserving K

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Schedulers - control invariants (2)

### A control invariant $K \Rightarrow K_0$

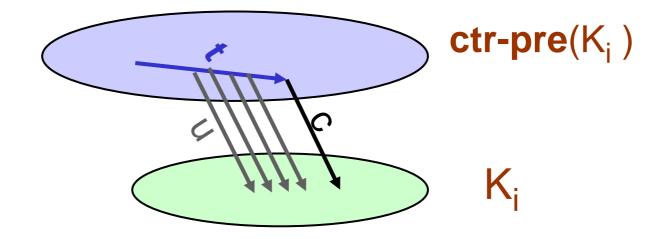




#### Schedulers - Control invariants : some results

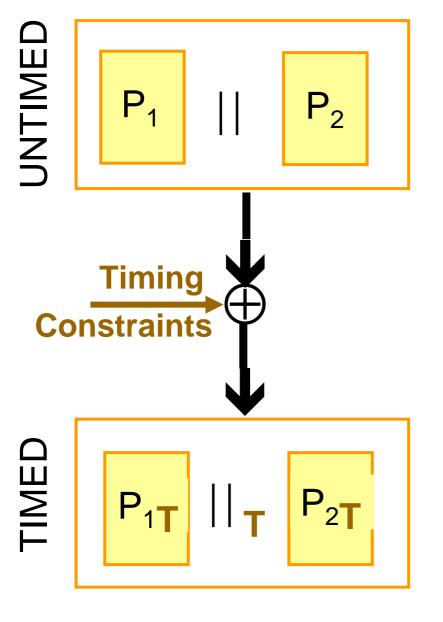
For any system S and timing constraints  $K_0$ 

- There exists a maximal control invariant K,  $K \Rightarrow K_0$
- K can be computed as the result of a synthesis semi-algorithm SYNTH(S,K<sub>0</sub>) =  $\lim_{I \in K_{i}} Where K_{i+1} = K_{i} \wedge ctr-pre(K_{i})$



• The conjunction of control invariants is not a control invariant conditions for composability of schedulers

### **Building timed models**



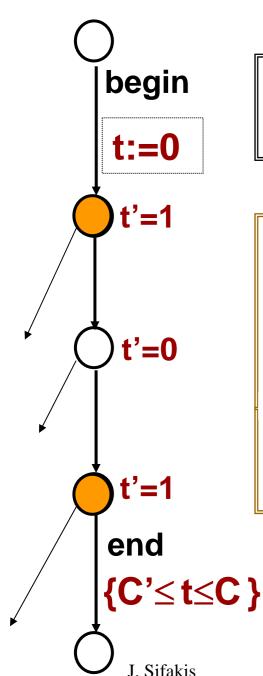
Methodology :

- Avoid over-specification which may lead to inconsistency
- Make explicit all the consequences of the constraints on actions (completeness)
- Define ||<sub>T</sub> so as to preserve properties such as welltimedness, and deadlockfreedom

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### **Building timed models**



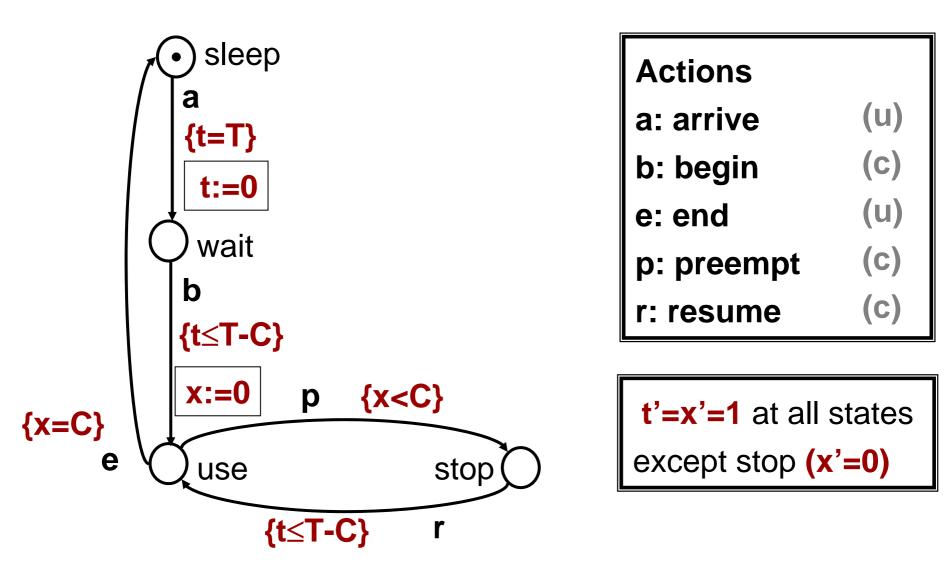
Automata: set of labeled transition on a set of actions

**Timers:** real-valued variables that can

- be reset (started) and tested at transitions
- increase (derivative =1) or remain unchanged at states (derivative =0)

Building timed models - example

A periodic process of period **T** and completion time **C** 



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Scheduler specification : K<sub>SCH</sub>

The scheduling constraint  $K_{SCH}$  relates timing constraints of 3 different kinds

• from the **execution platform** e.g. execution times, latency times

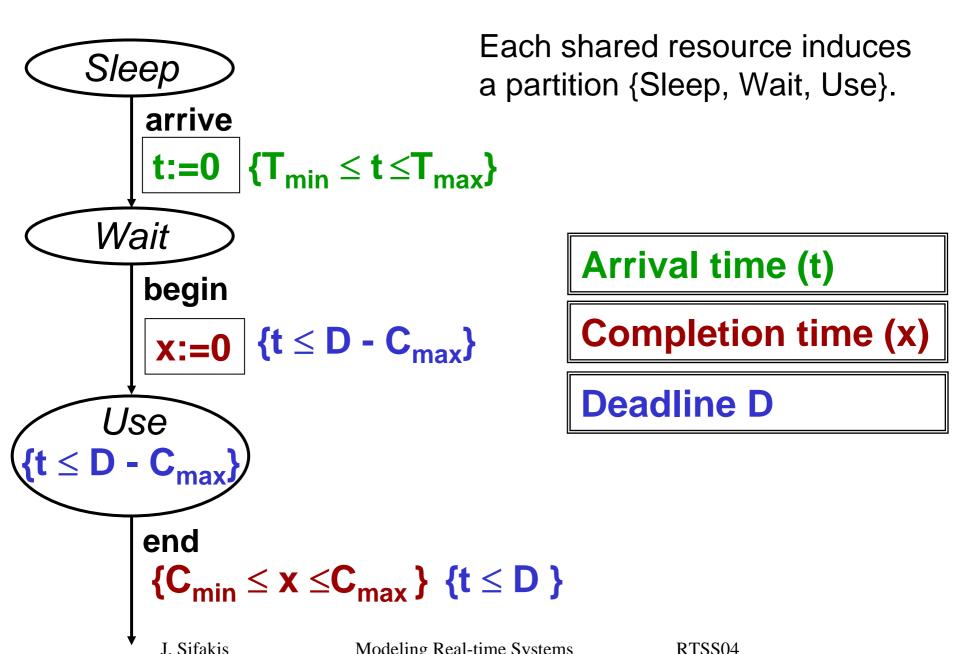
• from the **external environment** about arrival times of triggering events e.g. periodic tasks

• **user requirements** e.g. QoS, which are timing constraints relating events of the real-time system and events of its environment e.g. deadlines, jitter

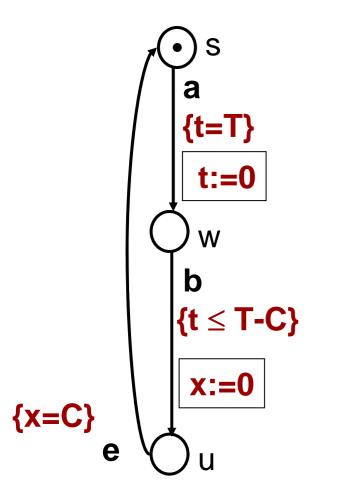
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Scheduler specification : K<sub>SCH</sub>



## Scheduler specification: K<sub>SCH</sub>



 $K_{SCH} = \bigwedge_{i} K^{i}_{SCH}$ where  $K^{i}_{SCH}$  expresses the property that no timing constraint is violated in process *i*.

For timelock-free process models with bounded guards schedulability boils down to deadlockfreedom of processes

$$\mathsf{K}_{\mathsf{SCH}} = \mathsf{S} \land (\mathsf{t} \leq \mathsf{T}) \lor \mathsf{W} \land (\mathsf{t} \leq \mathsf{T} \text{-} \mathsf{C}) \lor \mathsf{U} \land (\mathsf{x} \leq \mathsf{C})$$

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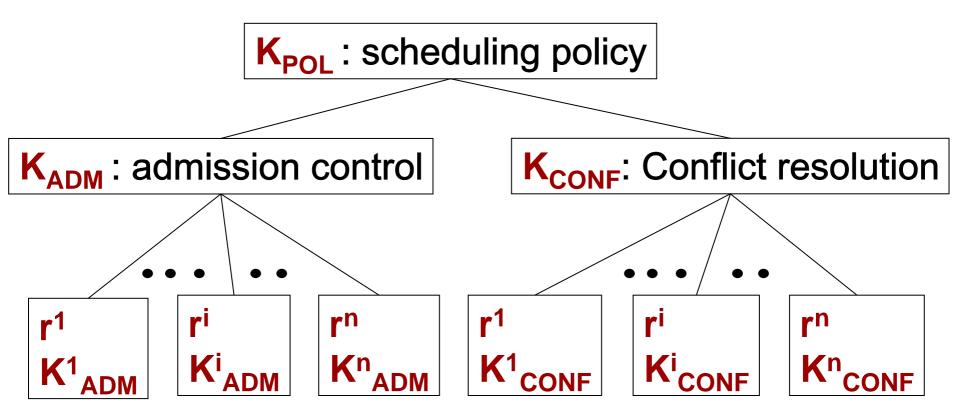
# Scheduler specification : **K**<sub>POL</sub>

 $K_{POL}$  is the conjunction of scheduling policies for the set R of shared resources

 $\mathbf{K}_{POL} = \bigwedge_{r \in \mathbf{R}} \mathbf{K}^{r}_{POL}$  where  $\mathbf{K}^{r}_{POL} = \mathbf{K}^{r}_{CONF} \wedge \mathbf{K}^{r}_{ADM}$ 

- K<sup>r</sup><sub>CONF</sub> says how conflicts for the acquisition of resource r are resolved e.g. EDF, RMS, LLF
- K<sup>r</sup><sub>ADM</sub> says which requests for r are considered by the scheduler at a state e.g. masking

# Scheduler specification: **K**<sub>POL</sub>



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Scheduler specification – K<sub>POL</sub> : example

# K<sub>POL</sub> for the Priority Ceiling Protocol

Admission control: *"Process P is eligible for resource r if the current priority of P is higher than the ceiling priority of any resource allocated to a process other than P"* 

Conflict resolution: " The CPU is allocated to the process with the highest current priority"

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# The modeling framework

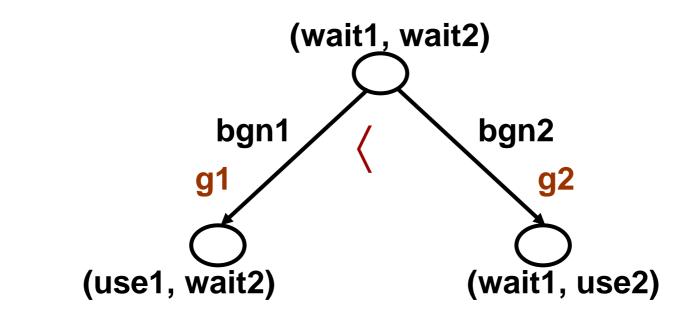
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Timed models with priorities



Priority rule	Strengthened guard of bgn1
true → bgn1 <b>〈</b> bgn2	g1' = g1 ∧ ¬g2
C → bgn1 〈 bgn2	g1' = g1 ∧ ¬(C ∧ g2)

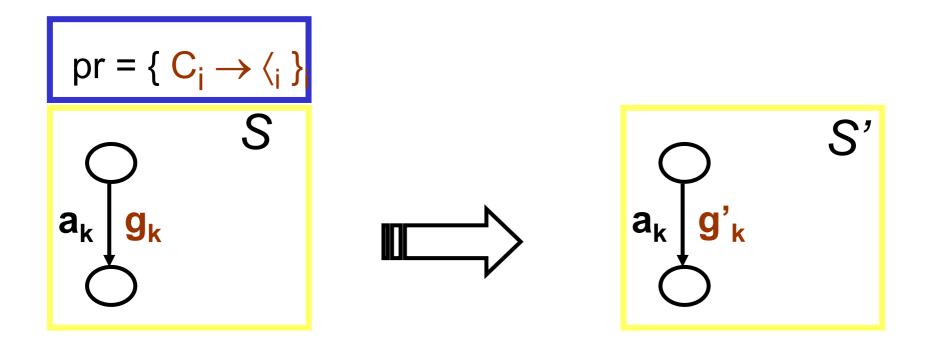
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#### Timed models with priorities

A priority order is a strict partial order,  $\langle \subseteq A^c \times A \rangle$ 

A set of priority rules, pr = {  $C_i \rightarrow \langle_i \rangle_i$  where { $C_i \rangle_i$  is a set of disjoint state predicates



$$\mathbf{g'_k} = \mathbf{g_k} \land \land \underset{C \to \langle epr}{} (C \Rightarrow \land_{ak \langle ai} \neg \mathbf{g})$$

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# Scheduling and Priorities - results

If **K** is a constraint characterizing a set of deadlockfree states of **S** then there exists a set of priority rules **pr** such that **(S,pr)** preserves **K** 

For any control invariant K of S there exists a set of dynamic priority rules **pr** such that the scheduled system S/K = (S, pr)

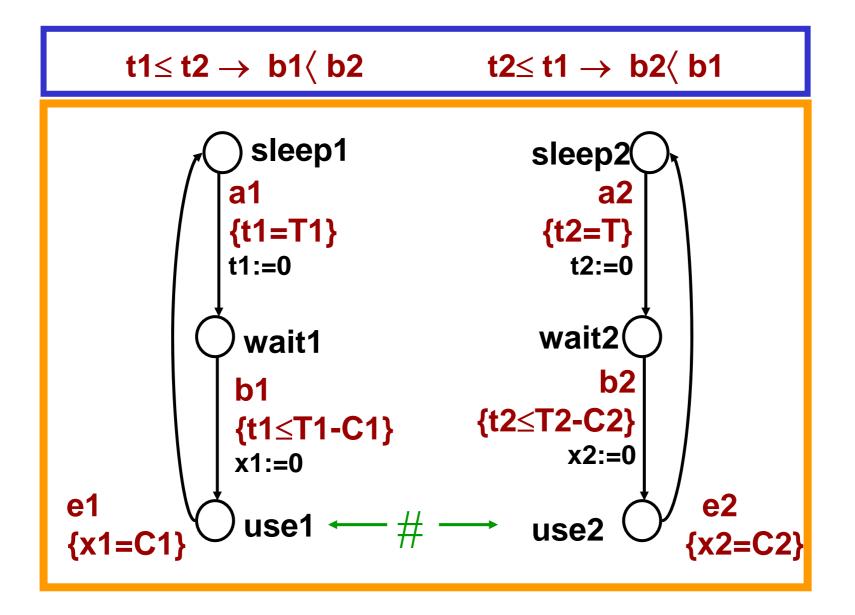
Any feasible scheduling policy  $K_{POL}$  induces a

restriction that can be described by dynamic priorities

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### Timed models with priorities: FIFO policy



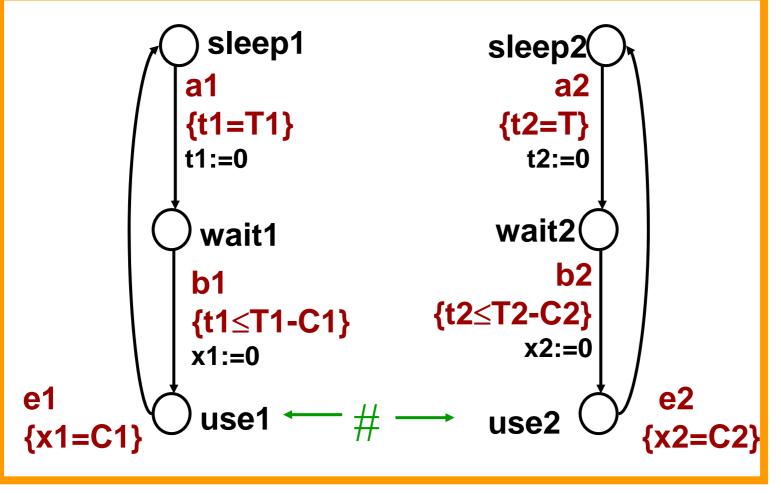
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Timed models with priorities : Least Laxity First policy



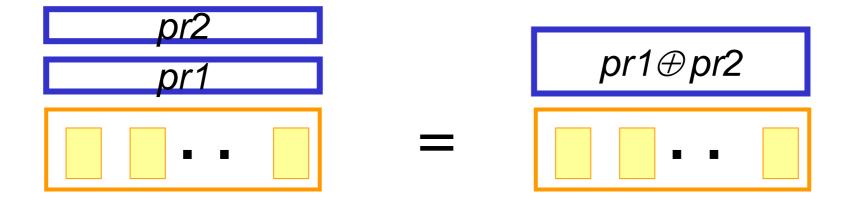
where Li =Ti-Ci-ti is the laxity of process i



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## Timed models with priorities: composition of priorities



 $pr1 \oplus pr2$  is the least priority order containing  $pr1 \cup pr2$ 

## **Results :**

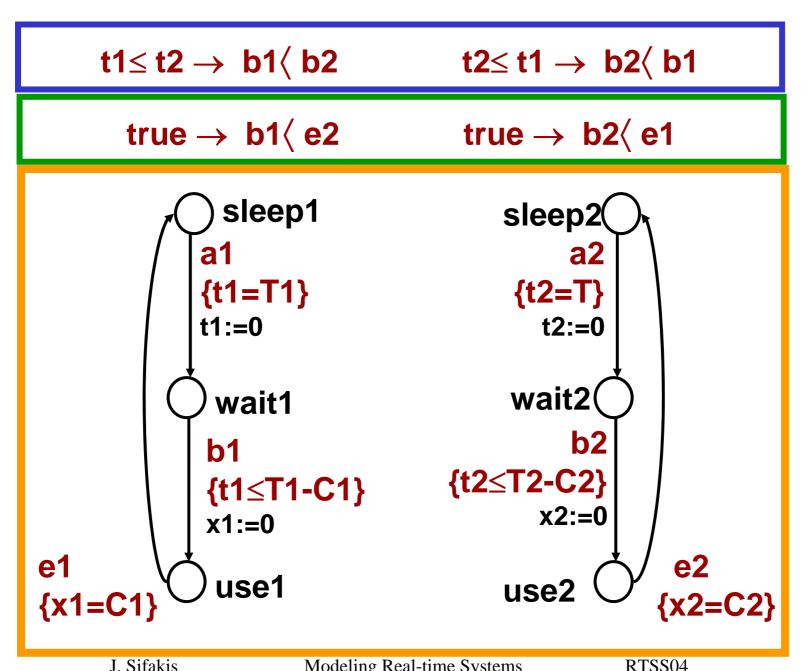
 $\succ$  The operation  $\oplus$  is partial, associative and commutative

Sufficient conditions for deadlock-freedom and liveness

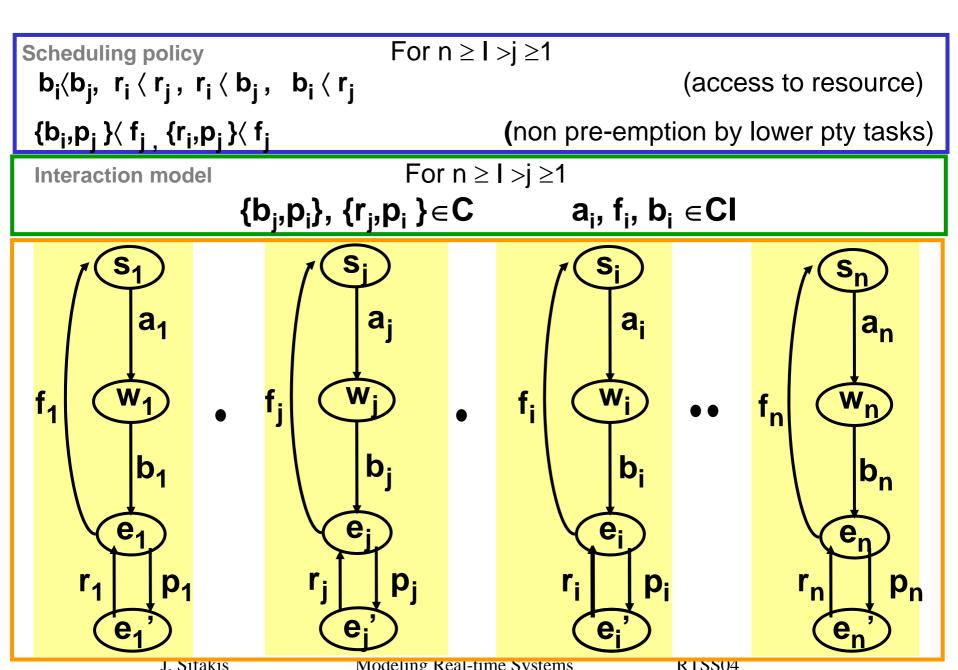
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#### Timed models with priorities: mutual exclusion + FIFO



### Timed models : Fixed priority preemptive scheduling



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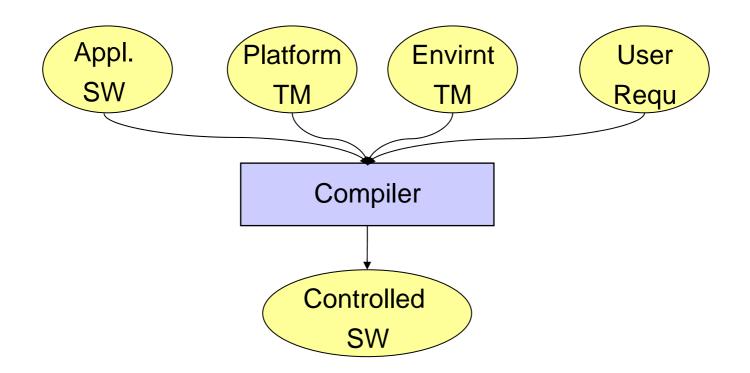
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### Discussion : SW is the model



 Specific and tractable methodology relying on a minimal set of constructs and principles e.g. interaction models + priorities

Layering  $\Rightarrow$  separation of concerns  $\Rightarrow$  incremental description

 Focus on specific construction principles and rules to ensure correctness constructively, especially for safety and deadlock-freedom L Sifakis Modeling Real-time Systems RTSS04

## Discussion : A framework for scheduling

- The controller synthesis paradigm is a basis for a general framework for scheduler specification ( $K_{SCH} \wedge K_{POL}$ ) and design (control invariants)
- Scheduling theory studies sufficient conditions guaranteeing K<sub>SCH</sub> for particular scheduling policies K<sub>POL</sub> and interaction models (architectures)
- Extending the model-based approach to encompass scheduler modeling and design
- Scheduler design methodology based on model-checking techniques – scheduling policies can be used to simplify the synthesis problem

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## **Discussion : Dynamic priorities**

That's all you need!

- they allow straightforward modeling of
  - urgency (priority of actions over time progress)
  - scheduling policies
  - schedulers (control invariants)
- run to completion and synchronous execution can be modeled by assigning priorities to threads (implemented in the IF toolset)
- Composability and compositionality results

### **Discussion : some papers**

#### THEORY

- "Scheduler modeling based on the controller synthesis paradigm" Journal of Real-time Systems, Vol. 23, pp.55-84, 2002
- "A Framework for Scheduler Synthesis" RTSS 1999
- "Component-based construction of deadlock-free systems", FSTTCS03, LNCS 2194,
- "Priority Systems" Proceedings of FMCO'03, LNCS 3188
- "Composition for component-based modeling", FMCO 02, LNCS 2852

### **APPLICATIONS**

- S. Yovine et al. "A methodology and tool support for generating scheduled native code for real-time Java applications" EmSoft 03
- "TAXYS: a tool for the development and verification real-time embedded systems" CAV'01. LNCS 2102.
- M. Bozga, S. Graf, II. Ober, Iul. Ober, J. Sifakis "The IF Toolset" Formal Methods for the Design of Real-Time Systems, Sept 2004, LNCS 3185